Service Manual



ORDER NO. RRV2615

DV-45ADV-656A

THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Model	Туре	Power Requirement	Reginal restriction codes (Region No.)	Remarks
DV-45A	KUXJ/CA	AC120V	1	
DV-656A	KUXJ/CA	AC120V	1	



PIONEER CORPORATION 4-1, Meguro 1-chome, Meguro-ku, Tokyo 153-8854, Japan PIONEER ELECTRONICS (USA) NIC. P.O. Bost 1760, Los Basch, CA 9081-1760, U.S.A. PIONEER EUROPE NI Haven 1087, Keatberglann 1, 912 Melseley, Belgium PIONEER ELECTRONICS ASIACENTRE PTE. LTD. 253 Alexandra Road, #01-01, Singapore 159936 (SPIONEER CORPORATION 2002)

SAFTY INFORMATION



This service manual is intended for qualified service technicians; it is not meant for the casual do-it-yourselfer. Qualified technicians have the necessary test equipment and tools, and have been trained to properly and safely repair complex products such as those covered by this manual.

Improperly performed repairs can adversely affect the safety and reliability of the product and may void the warranty. If you are not qualified to perform the repair of this product properly and safety, you should not risk trying to do so and refer the repair to a qualified service technician.

WARNING

This product contains lead in solder and certain electrical parts contain chemicals which are known to the state of California to cause cancer, birth defects or other reproductive harm.

Health & Safety Code Section 25249.6 - Proposition 65

NOTICE

(FOR CANADIAN MODEL ONLY)

Fuse symbols - (fast operating fuse) and/or - (slow operating fuse) on PCB indicate that replacement parts must be of identical designation.

REMARQUE

(POUR MODÈLE CANADIEN SEULEMENT)

Les symboles de fusible (tusible de type rapide) et/ou (tusible de type lent) sur CCI indiquent que les pièces de remplacement doivent avoir la même désignation.

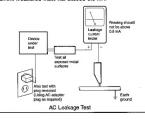
- (FOR USA MODEL ONLY) -

1. SAFETY PRECAUTIONS

The following check should be performed for the continued protection of the customer and service technician

LEAKAGE CURRENT CHECK

Measure leakage current to a known earth ground (water pipe, conduit, etc.) by connecting a leakage current tester such as Simpson Model 229-2 or equivalent between the earth ground and all exposed metal parts of the appliance (input/output terminals, screwheads, metal overlays, control shaft, etc.), Plug the AC line cord of the appliance directly into a 120V AC 60 Hz outlet and turn the AC power switch on. Any current measured must not exceed 0.5 mA.



ANY MEASUREMENTS NOT WITHIN THE LIMITS OUTLINED ABOVE ARE INDICATIVE OF A POTENTIAL SHOCK HAZARD AND MUST BE CORRECTED BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

2. PRODUCT SAFETY NOTICE

Many electrical and mechanical parts in the appliance have special safety related characteristics. These are often not evident from visual inspection nor the protection afforded by them ecosesarily can be obtained by using replacement components rated for voltage, at the contract of th

Electrical components having such features are identified by marking with a Δ on the schematics and on the parts list in this Service Manual.

The use of a substitute replacement component which does not have the same safety characteristics as the PIONEER recommended replacement one, shown in the parts list in this Service Manual, may create shock, fire, or other hazards.

Product Safety is continuously under review and new including an issued from time to time. For the latest information, always consult the current PIONEER Service Manual. A subscription to, or additional copies of, PIONEER Service Manual may be obtained at a nominal charge from PIONEER.

[Important symbols for good services]
In this manual, the symbols shown below indicate that adjustments, settings or cleaning should be made securely.
When you find the procedures bearing any of the symbols, be sure to fulfill them:

1. Product safety

5



You should conform to the regulations governing the product (safety, radio and noise, and other regulations), and should keep the safety during servicing by following the safety instructions described in this manual.

2. Adjustments



To keep the original performances of the product, optimum adjustments or specification confirmation is indispensable. In accordance with the procedures or instructions described in this manual, adjustments should be performed.



For optical pickups, tape-deck heads, lenses and mirrors used in projection monitors, and other parts requiring cleaning, proper cleaning should be performed to restore their performances.

4. Shipping mode and shipping screws



To protect the product from damages or failures that may be caused during transit, the shipping mode should be set or the shipping screws should be installed before shipping out in accordance with this manual, if necessary.

5. Lubricants, glues, and replacement parts



Appropriately applying greats or give can maintain the product performances. But improper lubrication or applying greats or give can maintain the product performances. But improper lubrication or applying give may lead to failures or results in the product. By following the instructions in this manual, be sure to apply the prescribed greats or give to proper portions by the appropriate amount For replacement parts or tools, the prescribed ones should be used.

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1. SPECIFICATIONS

General
SystemDVD Player
Power requirements AC 120V, 60 Hz
Power consumption
DV-45A 13 W
DV-656A12 W
Power consumption (standby)0.3W
Weight 2.6 kg (5lb 12oz)
Dimensions 420 (W) x 69 (H) x 278 (D) mm
(16 % (W) x 2 3/4 (H) x 11 (D) in.)
Operating temperature+5°C to +35°C
(+36°F to +96°F)
Operating humidity 5% to 85%
(no condensation)
Component Video output (Y, PB, PR)
Output level
P _B , P _R : 0.7 Vp-p (75Ω)
Jacks RCA Jacks
S-Video output
Y (luminance) - Output level 1 Vp-p (75 Ω)
Y (luminance) - Output level 1 Vp-p (75Ω) C (color) - Output level 286 mVp-p (75Ω)
Y (luminance) - Output level 1 Vp-p (75 Ω)
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Y (luminance) - Output level
$\begin{array}{lll} \text{Y (luminance)} & \text{-Output level.} & \text{-1 Vp-p } (75 \Omega) \\ \text{C (color)} & \text{-Output level.} & \text{-286 mVp-p } (75 \Omega) \\ \text{Jack} & \text{-SV/ideo jack} \\ \hline \textbf{Video output} \\ \text{Output level.} & \text{-1 Vp-p } (75 \Omega) \\ \text{Jack} & \text{-RCA jack} \\ \end{array}$
Y (luminance) - Output level
Y (luminance) - Output level
\(\frac{1}{\text{V(miniancs}} \) - Output level
Y (luminance) - Output level
Y (luminance) - Output level
Y (turninancs) - Output level
Y (tuminance) - Output level
Y (luminance) - Output level
Y (tuminance) - Output level

Accessories

 Stereo Audio Cable (VDE1052)
 Power Cable (ADG7022) (L = 1.5m)

Jacks RCA jack





 Video Cable (VDE1053) (L = 1.5m)



Digital audio characteristics

Frequency response

...... 4 Hz to 44 kHz(DVD fs: 96 kHz) S/N ratio118 dB Dynamic range108 dB Total harmonic distortion0.001% Wow and flutter Limit of measurement (0.001%W, PEAK) or lower

Digital output

Optical digital output Optical digital jack Coaxial digital outputRCA jack Other terminals

Control out Minijack (3.5 ø)

Accessories

Stereo audio cable.....1 Video cable1 Power cable 1 Remote control1 AA/R6P dry cell batteries 2 Operating Instructions1 Warranty card1

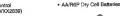
Note

· The specifications and design of this product are subject to change without notice, due to improvement.

> Manufactured under license from Dolby Laboratories, "Dolby" and the double-D symbol are trademarks of Dolby Laboratories. "DTS" is a registered trademark of Digital Theater Systems, Inc.

> • TruSurround and the () * symbol are trademarks of SRS Labs, Inc. TruSurround technology is incorporated under license from SRS Labs, Inc.

 Remote Control (DV-45A: VXX2839)





 Remote Control (DV-656A: VXX2800)

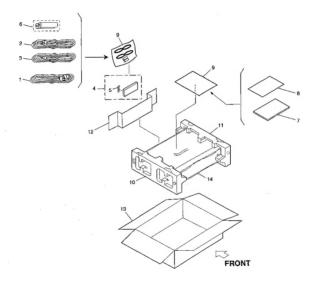




2. EXPLODED VIEWS AND PARTS LIST

- NOTES:
 Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.
 The A mark found on some component parts indicates the importance of the safety factor of the part. The A mark found on some component parts indicates the importance of ine superfying Therefore, when replacing, be sure to use parts of identical designation.
 Screws adjacent to \(^V\) mark on product are used for disassembly.
 For the applying amount of lubricants or glue, follow the instructions in this manual.
 In the case of no amount instructions, apply as you think it appropriate.)

2.1 PACKING



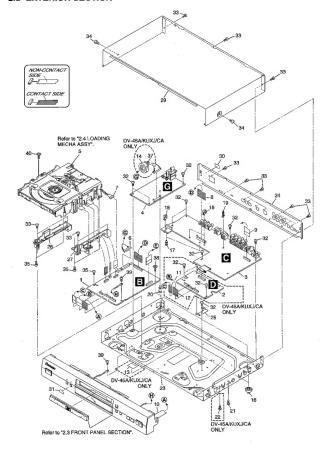
PACKING parts List

Mark	No	Description	Part No.	Mark No.	Description	Part No.
ALL CONTRACTOR OF THE PARTY OF			NSP 8		Warranty Card	ARY7045
A	1	Power Cable	ADG7022	9	Polyethylene Bag	VHL1051
	2	Stereo Audio Cable (L = 1.5m)	VDE1052			
	3	Video Cable (L = 1.5m)	VDE1053	10	Pad L	VHA1307
	4	Remote Control	See Contrast table (2)	11	Pad R	VHA1308
	5	Battery Cover	See Contrast table (2)	12	Paper Board	VHC1096
		Dullory Corton		13	Packing Case	See Contrast table (2)
NSP	6	AA/R6P Dry Cell Battery	VEM1031			
	7	Operating Instructions	See Contrast table (2)	14	Mirror Mat Sheet	Z23-007
		(English)				

(2) CONTRAST TABLE
DV-45A/KUXJ/CA and DV-656A/KUXJ/CA are constructed the same except for the following:

Mark	No.	Symbol and Description	DV-45A/KUXJ/CA	CA CA
-	4	Bemote Control	VXX2839	VXX2800
		Battery Cover	VNK4423	VNK4997
		Operating Instructions (English)	VRB1297	VRB1296
		Packing Case	VHG2224	VHG2222

2.2 EXTERIOR SECTION



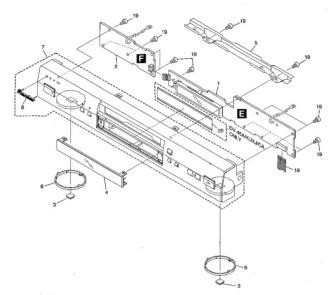
EXTERIOR SECTION parts List

Mark No.	Description	Part No.	Mark No.	Description	Part No.	
1	DVDM Assy	See Contrast table (2)				
2	SACDB Assy	See Contrast table (2)	21	PCB Holder	VEC2283	- 1
3	JACB Assy	See Contrast table (2)	22	PCB Holder	See Contrast table (2)	
	•		NSP 23	Base Chassis	VNA2521	
A 4	POWER SUPPLY Unit	VWR1351	24	Rear Panel	See Contrast table (2)	
NSP 5	LOADING MECHA Assy	VWT1196	NSP 25	PCB Base	VNE2276	
						- 1
6	Connector Assy	PF13PP-D25	26	Adapter 14L	VNL1941	
7	Connector Assy	PG05KK-E30	27	Adapter 14R	VNL1942	
8	FFC (30P, JACB)	VDA1905	. 29	Bonnet Case S	See Contrast table (2)	
9	FFC (21P, JACB)	VDA1906	NSP 30	ID Label	VRW1877	
10	FFC (17P, FLKB)	VDA1907				
			NSP 31	Energy Star Label	AAX7876	
11	FFC (20P, DSP)	See Contrast table (2)	32	Screw	BBZ30P060FMC	
12	FFC (40P, SACD)	See Contrast table (2)	33	Screw	BBZ30P080FZK	
13	F Cushion	See Contrast table (2)	34	Screw	See Contrast table (2)	
14	Ferrite Core	See Contrast table (2)	35	Screw	PPZ30P080FMC	
15	*****					,
			36	****		
16	LEG Assy SX	AEC7113	NSP 37	Binder	See Contrast table (2)	
NSP 17	PCB Spacer (3 x 6)	AEC7156	38	Screw	IBZ30P080FCC	
18	Mini Clamp	AEC7373	39	Screw	BBZ30P060FCC	
NSP 19	PCB Support	REC1285	40	Screw	Z39-019	,
20	PCB Support	VEC2184				,

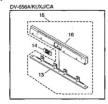
(2) CONTRAST TABLE DV-45A/KUXJ/CA and DV-656A/KUXJ/CA are constructed the same except for the following:

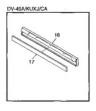
Mark	No.	Symbol and Description	DV-45A/KUXJ/CA	DV-656A/KUXJ CA
	1	DVDM Assy	VWS1533	VWS1531
	2	SACDB Assy	VWG2352	Not used
	3	JACB Assy	VWV1912	VWV1913
	11	FFC (20P, DSP)	VDA1909	Not used
	12	FFC (40P, SACD)	VDA1910	Not used
	13	F Cusion	VEB1348	Not used
	14	Ferrite Core	VTH1044	Not used
	22	PCB Holder	VEC2283	Not used
	24	Rear Panel	VNA2463	VNA2417
	29	Bonnet Case S	VXX2842	VXX2841
	34	Screw	BCZ40P060FZK	BCZ40P060FN
USP	37	Binder	ZCA-BK1	Not used

2.3 FRONT PANEL SECTION



• Tray Panel Section







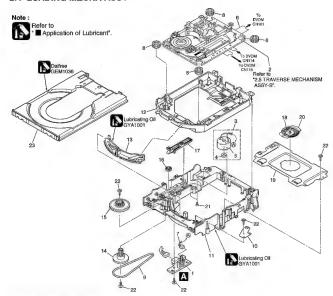
FRONT PANEL SECTION parts List

Mark No.	Description	Part No.	Mark No.	Description	Part No.
1	FLKY Assv	See Contrast table (2)	11	****	
2	KEYB Assv	VWG2377	12	****	
3	Rubber Foot	VEB1325	13	Sub Panel	See Contrast table (2)
4	FL Lens	See Contrast table (2)	. 14	DVD A/V Badge	See Contrast table (2)
5	FP Angle	VNE2267	15	Tray Panel Assy	See Contrast table (2)
6	Ring	VNK4996	16	Tray Panel	See Contrast table (2)
7	Front Panel Assv	See Contrast table (2)	17	Door	See Contrast table (2)
8	Pioneer Badge	See Contrast table (2)	18	FFC (17P, FLKB)	VDA1907
9	*****		19	Screw	BBZ30P100FZK
10	*****		20	FL Filter	See Contrast table (2)

(2) CONTRAST TABLE DV-45A/KUXJ/CA and DV-656A/KUXJ/CA are constructed the same except for the following:

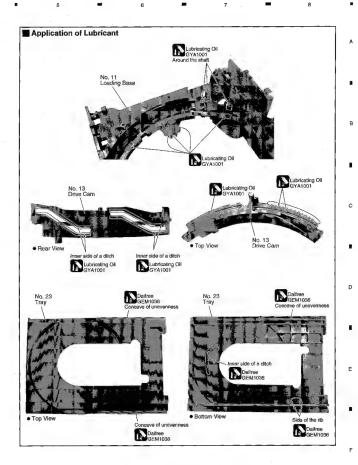
Mark	No.	Symbol and Description	DV-45A/KUXJ/CA	DV-656A/KUXJ/ CA
	1	FLKY Assy	VWG2354	VWG2376
	4	FL Lens	VEC2277	VNK5028
	7	Front Panel Assy	VXA2517	VXA2515
	8	Pioneer Badge	VAM1109	VAM1129
	13	Sub Panel	Not used	VNK5023
	14	DVD A/V Badge	Not used	VAM1131
	15	Tray Panel Assy	Not used	VXA2518
	16	Tray Panel	VNK5021	VNK5022
	17	Door	VEC2279	Not used
	20	FL Filter	VEC2280	Not used

2.4 LOADING MECHA ASSY

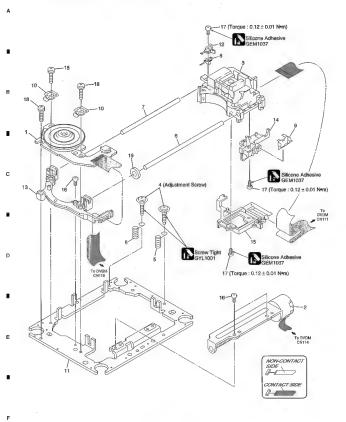


LOADING MECHA ASSY parts List

Mark No.	Description	Part No.	Mark No.	Description	Part No.
NSP 1	LOAB Assy	VWG2346	16	Drive Gear	VNL1923
2	Traverse Mechanism Assv-S	VXX2782	17	SW Lever	VNL1925
3	Loading Motor Assy	VXX2505	18	Clamper Plate	VNE2251
4	Motor Pulley	PNW1634	19	Bridge	VNE2252
5	Carriage DC Motor / 0.3W	PXM1027	20	Clamper	VNL1924
6	Flexible Cable (26P)	VDA1864	21	Screw	JGZ17P028FMC
7	Connector Assy 2P	VKP2253	22	Screw	Z39-019
8	Float Rubber	VEB1327	23	Tray	VNL1920
9	Belt	VEB1330			
10	Stabilizer	VNE2253			
11	Loading Base	VNL1917			
12	Float Base DVD	VNL1918			
13	Drive Cam	VNL1919			
14	Gear Pulley	VNL1921			
15	Loading Gear	VNL1922			



2.5 TRAVERSE MECHANISM ASSY-S

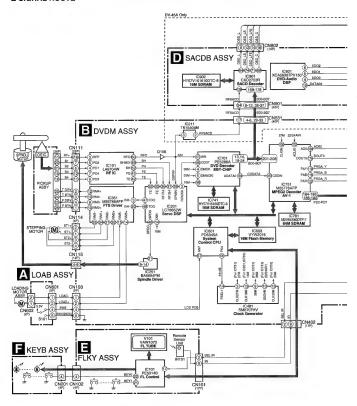


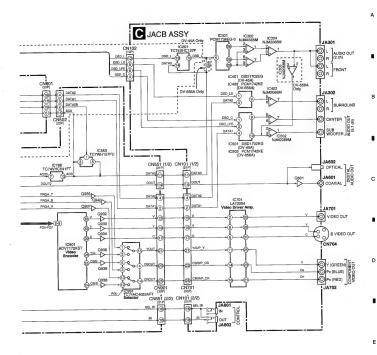
TRAVERSE MECHANISM ASSY-S parts List

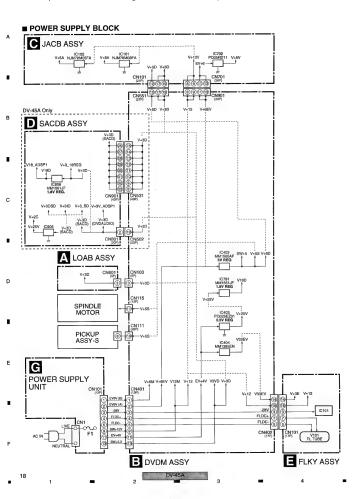
Mark No.		Description	Part No.
1		Spindle Motor	VXM1088
2	2	Stepping Motor	VXM1090
∆ 3	3	Pickup Assy-S	OXX8003
4	ŀ	Skew Screw	VBA1080
5	5	Skew Spring	VBH1335
ε	3	Guide Bar	VLL1514
7	,	Sub Guide Bar	VLL1515
8	3	Hold Spring	VNC1017
9	9	Joint Spring	VNC1019
11	0	Support Spring	VNC1020
NSP 1	1	Mechanism Chassis	VNE2248
10	2	Slider	VNL1811
1:	3	Spacer	VNL1913
1.	4	Joint	VNL1914
1:	5	FFC Holder	VNL1915
1	6	Screw	BBZ20P050FZK
17		Tapping Screw	OBA8009
1	8	Screw	PMA26P100FMC
19		Damper Sheet	VEB1335

3. BLOCK DIAGRAM AND SCHEMATIC DIAGRAM 3.1 BLOCK DIAGRAM

■ SIGNAL ROUTE

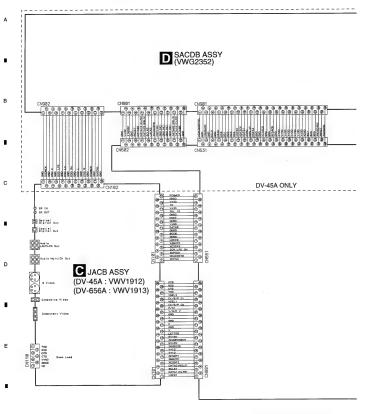




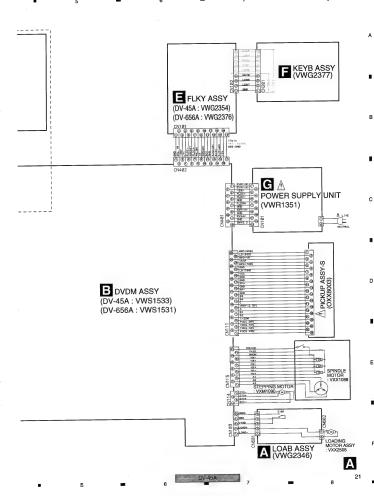


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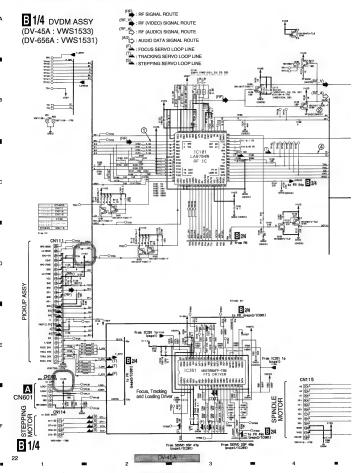
3.2 LOAB ASSY and OVERALL WIRING DIAGRAM

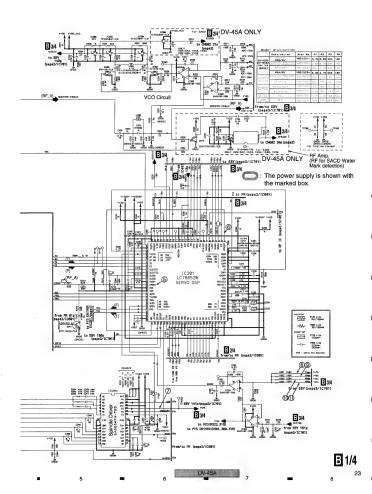


Note: When ordering service parts, be sure to refer to "EXPLODED VIEWS and PARTS LIST" or "PCB PARTS LIST".

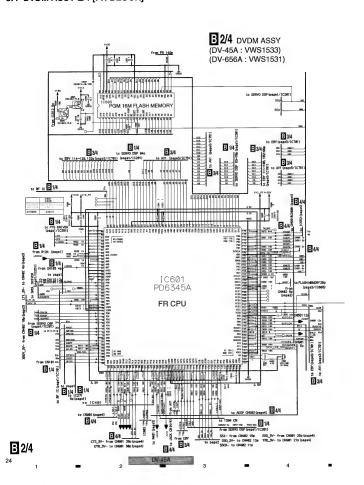


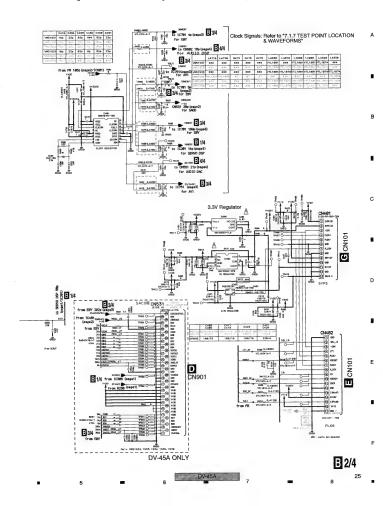
3.3 DVDM ASSY 1/4 [FTS BLOCK]



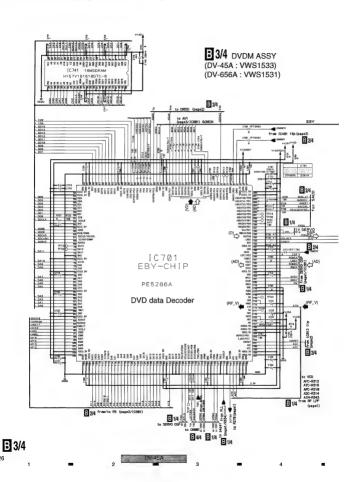


3.4 DVDM ASSY 2/4 [FR BLOCK]



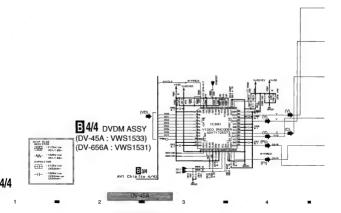


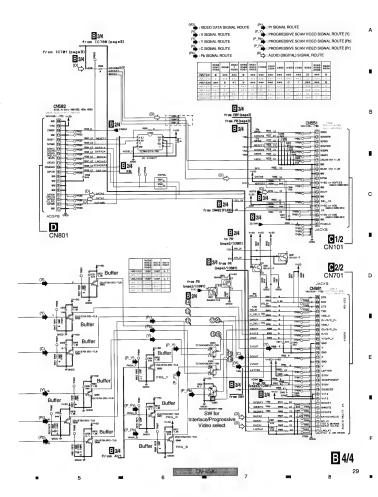
3.5 DVDM ASSY 3/4 [EBY/AV1 BLOCK]



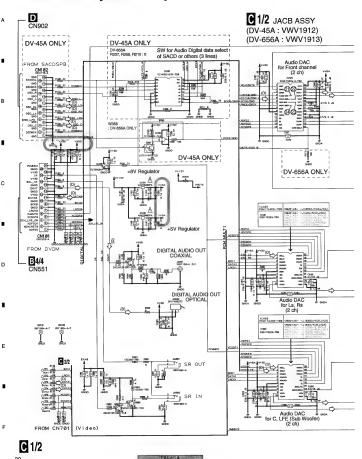
: The power supply is shown with the marked box. E 2/4 ē aaada aada 49 IC751 MITSUBISHI AV-1 M65776AFP MPEG, DVD-Audio, DTS Decoder and Progressive scan Processer RF (VIDEO) SIGNAL ROUTE : VIDEO DATA SIGNAL ROUTE AUDIO (DIGITAL) SIGNAL ROUTE ROGRESSIVE SCAN VIDEO SIGNAL ROUTE [Y] ROGRESSIVE SCAN VIDEO SIGNAL ROUTE [Pb] PROGRESSIVE SCAN VIDEO SIGNAL ROUTE [Pr]

3.6 DVDM ASSY 4/4 [VENC BLOCK]





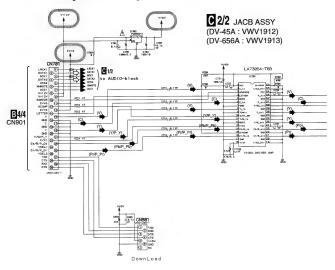
3.7 JACB ASSY 1/2 [AUDIO BLOCK]



(D) : AUDIO (DIGITAL) SIGNAL ROUTE SW for Mute Control Signal : AUDIO SIGNAL ROUTE Q350, Q351, Q360, Q361: Mute SW Differential Amp. IC302, IC303: I to V change circuit for Mute Control Circuitry to do LPF of a low level ingredient from Front L, R in order to add it to LFE SW for Mute Control DV-656A ONLY DV-45A C371→ R3371 (0) generation for Audio Amp Q410, Q420, Q510, Q520: Mute SW Audio Amp. (with LPF) Ls ch for Mute Control Signal Rs ch. LFE di. (Sub Woofer) Audio out Audio Amp. (with LPF) : The power supply is shown with the marked box. for Mute Control Signal C 1/2 LPF: Low Pass Filter

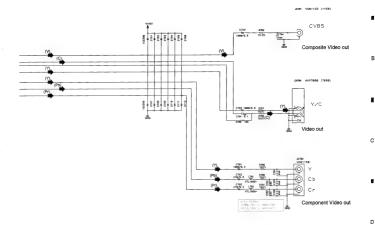
С

3.8 JACB ASSY 2/2 [VIDEO BLOCK]

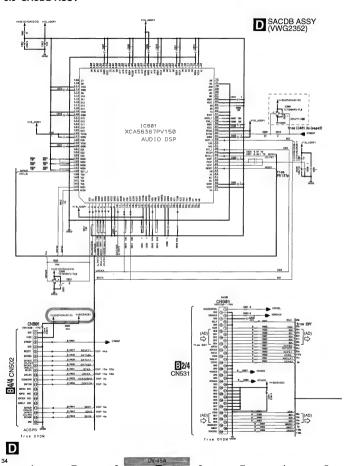


V SIGNAL ROUTE
SI

: The power supply is shown with the marked box.



3.9 SACDB ASSY

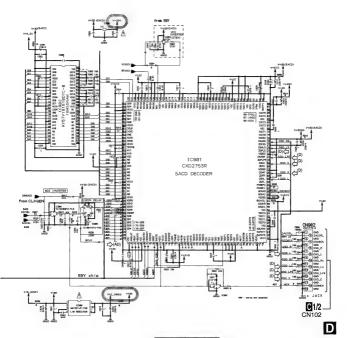


3

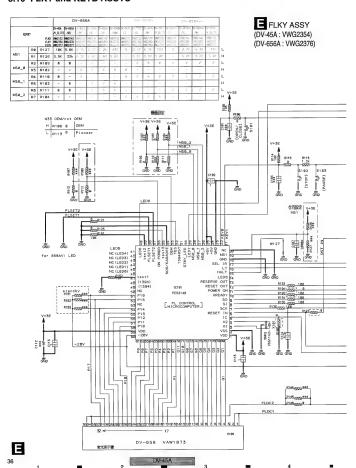
: The power supply is shown with the marked box.

(AD); : AUDIO DATA SIGNAL ROUTE

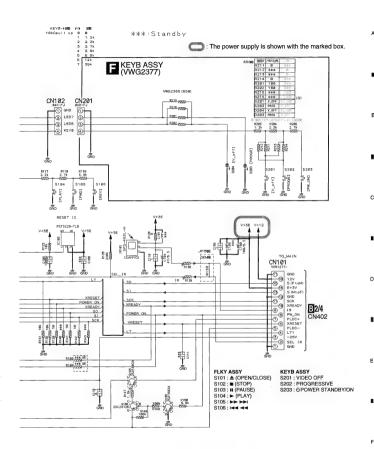
(D) : AUDIO (DIGITAL) SIGNAL ROUTE



3.10 FLKY and KEYB ASSYS



3

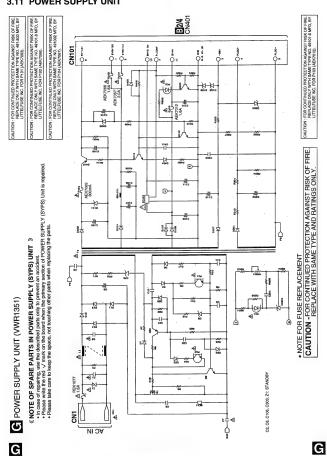


3.11 POWER SUPPLY UNIT

1

С

D



3.12 WAVEFORMS [DVDM ASSY]

Note: The encircled numbers denote measuring point in the schematic diagram.

B DVDM ASSY

Measurement condition: No. 1 to 4 and 6 to 11: MJK1, Title 1-chp 1
No. 5 : CD. ABEX-784 Track 1 No. 12 to 14 : DVD-REF-A1, T2-Chap.1 No. 15 to 20 : DVD-REF-A1, T2-Chap.19 7 IC251 - pin 24 (FG) V: 1V/div. H: 5mseq 1 IC101-pin 3 (RF) V: 200mV/div. H: 0.1s 13 Foot of R963 (C) V: 0.2V/div. H: 10s 19 Foot of R958 (Pb) V: 0.2V/div. H: 10µsec/div - GND Foot of R960 (Y)
V: 0.2V/div. H: 10 8 Foot of R261 (FPWM) V: 1V/div. H: 10µsec/d (3) 15 Foot of R959 (Y) V: 0.2V/div. H: 10 IC101-pin 42 (Tracking Error) (Al-Inner Tracking Off) V: 500mV/div. H: 2msec/div. 10 Foot of R263 (PPWM) V: 1V/dlv, H: 0,2msec 16 Foot of R958 (Pb) AC mode 5 IC201 - pin 39 (EFM before slice) V: 0.5V/div. H: 0.2usec/div Foot of R284 (RPWM) V: 1V/div. H: 5msec/div Foot of R957 (Pr) V: 0.2V/div. H: 10µseo/div 10- 5 10 6 IC201 - pin 1 (EFM) V: 1V/div. H: 0.2µseo/div 12 Foot of R984 (V) V: 0.2V/div. H: 10

3.13 WAVEFORMS [JACB ASSY]

Note: The encircled numbers denote measuring point in the schematic diagram.

C JACB ASSY

Measurement condition : No. 21 to 25 : DVD-REF-A1, T2-Chap.1

Measurement condition: No. 21 to

© 1001-pine (ECN)

V 1 Video H Spendale

© 1001-pine (IDATAI)

V 1 Video H Spendale

23 IC301-pin 6 (BCK) V: 1V/div. H: 100nsec/o

(24) IC301-pin 7 (PMCK)
V: 1V/div. H: 20nseoldiv.

D

25 IC302 - pin 1 (Analog OUT) V: 1V/div. H: 500µsec/div.

E

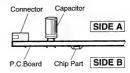
4. PCB CONNECTION DIAGRAM 4.1 LOAB ASSY

NOTE FOR PCB DIAGRAMS:

- Part numbers in PCB diagrams match those in the schematic diagrams.
- A comparison between the main parts of PCB and schematic diagrams is shown below.

diagrams is shown bolow.				
Symbol In PCB Diagrams	Symbol In Schematic Diagrams	Part Name		
G G G		Transistor		
000 BCE		Transistor with resistor		
000 DGS	ÜÜ	Field effect transistor		
<u>©000000</u> 00	*******	Resistor array		
000		3-terminal regulator		

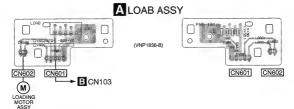
- The parts mounted on this PCB include all necessary parts for several destinations.For further information for respective destinations, be sure to check with the schematic diagram.
- View point of PCB diagrams.



SIDE A

SIDE B

С

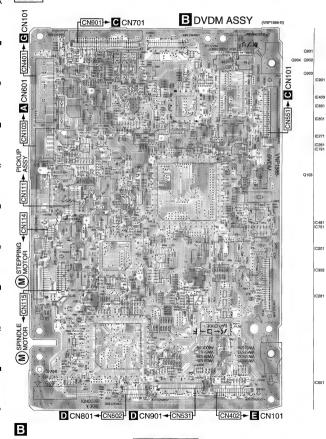




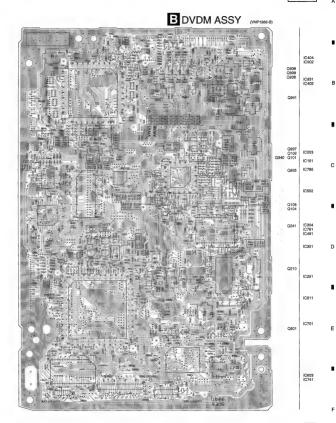


4.2 DVDM ASSY





SIDE B

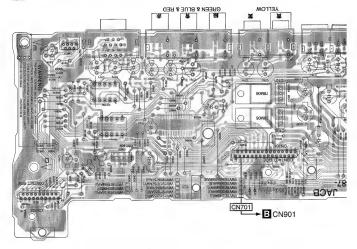


В

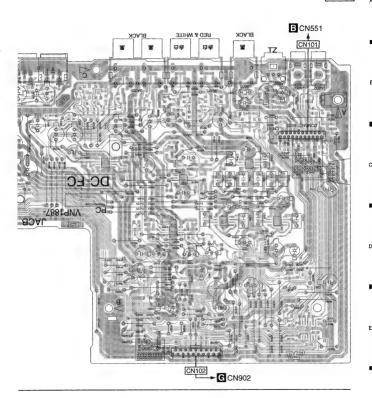
4.3 JACB ASSY

SIDE A

C JACB ASSY



IC702 IC101 IC102

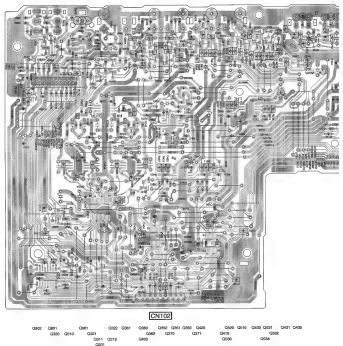


C

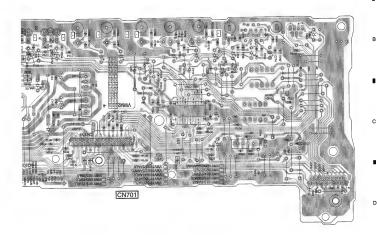
SIDE B

C JACB ASSY (VNP1887-C)

CN101



| IC302 | IC304 | IC305 | IC402 | | | IC202 | IC301 | IC303 | IC403 | IC401

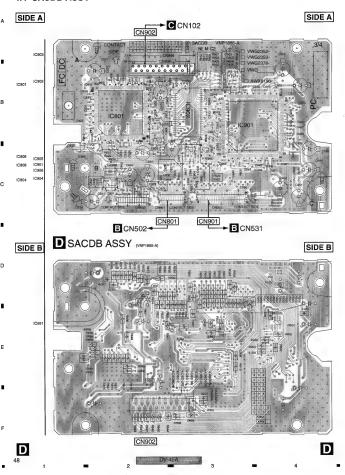


Q701

IC701

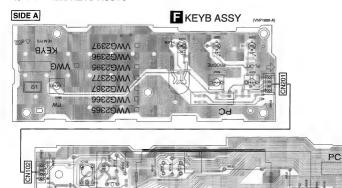
C

4.4 SACDB ASSY

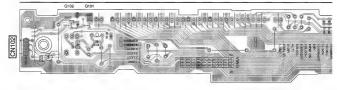


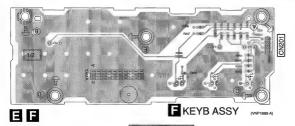
Е DV-45A 5

4.5 FLKY and KEYB ASSYS

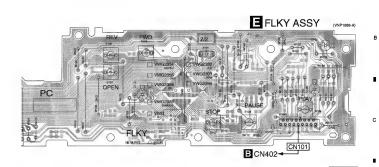


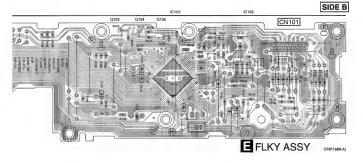
SIDE B



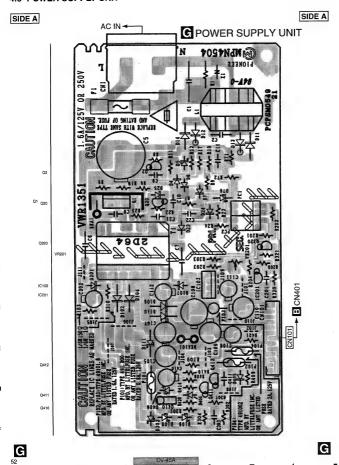


SIDE A





4.6 POWER SUPPLY UNIT



5. PCB PARTS LIST

- NOTES: Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.
 - The A mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.

smeapure, wan repacing, se sure to use parts of identical aesignation.

When ordering resistors, first covert resistance values into code form as shown in the following examples.

Ex. I When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47k ohm (tolerance is shown by J=5%, and K=10%).

560 Ω	-	56 x 101	-	561		RD1/4PU 56 I J
47k Ω	-	47 x 103	-	473		RD1/4PU473J
0.5 Ω	-	R50				RN2H R 500 K
1Ω		1R0				RSIP I R O K
L 4h ama	2 06	Tective dies	te (euc	has	in high precision metal film resistors	}

Ex.2 When there -- RN1/4PC 516 2 1 F 5.62k Ω → 562 x 101 → 5621 ····

Mark	No. Description	Part No.	Mark No. Description	Part No.
	OF ASSEMBLIES		IC101	LA9704W
DV-45			IC201	LC78652W
	1LOADING MECHA ASSY	VWT1196	IC781	M2V64S40DTP-7
NSP	2. LOAB ASSY	VWG2346	IC351	M56788AFP
1401	ZLOAD ACCT	***************************************	IC751	M65776AFP
	1. DVDM ASSY	VWS1533		
	IDVDM ASST	V W 3 1033	⚠ IC404	MM1385EN
	1JCSB ASSY	VWM2144	/î\ IC791	MM1561JF
	2JACB ASSY	VWV1912	/ IC402	MM1565AF
			IC601	PD6345A
	1SACDB ASSY	VWG2352	IC701	PE5286A
			/\ IC403	PQ025EZ01ZP
	1FLKB ASSY	VWM2132	∑ IC403	- GOZOLZO IZI
	2.FLKY ASSY	VWG2354	IC481	SM8707HV
	2KEYB ASSY	VWG2377		
	2KETB A551	VVVG23//	IC931	TC74HC4053AFT
		VWR1351	IC786	TC74VHC541FT
Δ	1POWER SUPPLY UNIT	VWH1351	IC303, IC304, IC306	TC7SZU04F
			IC553	TC7WH157FU
DV-65	6A		IC211	TK15404M
NSP	1LOADING MECHA ASSY	VWT1196	IC603	VYW2016
NSP	2LOAB ASSY	VWG2346	Q210, Q932-Q940	2SA1576A
			Q241	DTC114EUA
	1DVDM ASSY	VWS1531	Q101, Q102, Q106	HN1A01F
			Q101, Q102, Q100	1111111111
	1JCSB ASSY	VWM2145	Q103, Q104	HN1B04FU
	2JACB ASSY	VWV1913	O931	RN1911
			Q601, Q941	RN4982
	1FLKB ASSY	VWM2143	D302, D303	KV1470
	2FLKY ASSY	VWG2376	D401, D402	RB051L-40
	2KEYB ASSY	VWG2377	D401, D402	TIDOOTE-TO
	ZKE ID AGO!	***************************************	D601	BB501V-40
Δ	1POWER SUPPLY UNIT	VWR1351	D601	NB301V-40
			COILS AND FILTERS	
			1.304	LCYA1R2J2520
Mark	No. Description	Part No.	L4080, L4090, L4100 CHIP BEADS	VTL1074
Ware	No. Description	Fait NO.	L4110, L4120 CHIP BEADS	VTL1074
Λ	LOAB ASSY		L4130, L4820, L4880 CHIP BEADS	VTL1074
			L4910, L4920 CHIP BEADS	VTL1074
SWI	TCHES AND RELAYS		L4910, L4920 CHIF BEADS	VILIO
	01 BEAF SWITCH	VSK1011	L4930, L8020 CHIP BEADS	VTL1074
				VTL1079
отн	EDC		L4870 CHIP BEADS	
		S2B-PH-K	L4830, L4890, L4900 CHIP BEADS	VTL1081
	N602 CONNCTOR		L4800, L481 CHIP BEADS	VTL1084
	V601 CONNCTOR	S5B-PH-K		
- 1	PRINTED CIRCUIT BOARD	VNP1836	CAPACITORS	
			C474, C480, C481, C662	CCSRCH100D50
10	TVD11 4 00V D 0104	.001	C121, C532, C950, C953-C955	CCSRCH101J50
ы	DVDM ASSY [VWS15	33]	C314, C798	CCSRCH150J50
SEM	ICONDUCTORS			CCSRCH151J50
	801	ADV7172KST	C100, C133	CCSRCH181J50
	261, IC302	BA4510F	C120	003H0H101300
		BA6664FM	0.01.0.00.0.00.0.00	00000011000150
	251	HY57V161610DTC-8	C484, C485, C487, C491	CCSRCH220J50
IC	741	H19/4 ID IO IO IO IO-8	C134, C324, C391, C392	CCSRCH331J50

ark No. De	scription	Part No.	Mark No. I	Description	Part No.
C945, C946		CCSRCH331J50	R631, R713		RAB4C103J
C109		CCSRCH391J50	R111		RAB4C220J
C297		CCSRCH470J50	R113, R534, R537,	R704, R705	RAB4C470J
			R138		RS1/10S0R0J
C241		CCSRCH560J50	R341		RS1/10S101J
C107, C360		CCSRCH681J50			
C488, C490		CCSRCH820J50	R141-R148		RS1/10S220J
C489		CCSRCH8R0D50	R973, R978		RS1/16S1000F
C117, C123, C128, C	201, C233	CEV101M16	R364, R369, R373, R123	R375	RS1/16S1003F RS1/16S1202F
C254, C368, C369, C	103, C405	CEV101M16	R936, R944, R950,	, R966	RS1/16S1500F
C411, C413, C414, C4	119, C422	CEV101M16			
C801		CEV101M16	R358, R361		RS1/16S1503F
C103		CEV220M16	R755		RS1/16S1801F
C119, C205, C326, C	121, C424	CEV221M4	R956, R971, R979		RS1/16S2200F
		OF 1004114	R754		RS1/16S3001F RS1/16S3301F
C470, C472, C601, C		CEV221M4	R751		HS1/1053301F
C701, C702, C711, C		CEV221M4	R132		RS1/16S4702F
C751, C752, C766, C	/81, C/91	CEV221M4			RS1/16S6800F
C793		CEV221M4	R810, R817	Dago Dago	RS1/16S6800F
C101		CEV470M6R3	R357, R362, R363,	H308, H372	RS1/16S6802F
0440 0400 0000 0		OKOONDAOEKAO	R374		RS1/16S6802F VCN1127
C116, C127, C223, C		CKSQYB105K10	R257 (R=1.0)		VCN1127
C312, C406, C407, C	115, C416	CKSQYB105K10	DOED DOEC (P. C.C.	w.	VCN1128
C477, C794, C795	107 0504	CKSQYB105K10	R258, R259 (R=2.2	J	VCN1128 RS1/16S###J
C216, C313, C351, C4		CKSRYB102K50 CKSRYB102K50	Other Resistors		no I/ 100###J
C533, C534, C606, C	17, 0621	CKSHYB102K50	OTHERS		
C703, C748, C817, C	110 0051	CKSRYB102K50	OTHERS		0.00 01.010
		CKSRYB103K50	CN401 PH CONN		S13B-PH-SM3
C110, C113, C203, C2 C234, C261, C320-C3	20, 0225	CKSRYB103K50	CN103 CONNECT		S5B-PH-SM3
C404, C426, C619	22, 0330	CKSRYB103K50	9006 FLEXIBLE C		VDA1681 VKN1409
C108, C111, C114, C	115	CKSRYB104K16	CN114 4P CONN		VKN1416
			CN115 12P CON		
C212, C213, C227, C		CKSRYB104K16	CN402 17P CONF		VKN1421
C248-C251, C255, C	263, C315	CKSRYB104K16	CN551 21P CONF		VKN1425
C317		CKSRYB104K16	CN901 30P CONI		VKN1434
C106		CKSRYB152K50	CN502 20P CON		VKN1460
C208		CKSRYB222K50	CN111 26P CON	4ECTOR	VKN1790
C266		CKSRYB224K10	CN531 FFC CON	NECTOR	VKN1794
C206, C214, C242, C		CKSRYB472K50	KN1, KN2 EARTH	METAL FITTING	VNF1109
C105, C118, C122, C		CKSRYF104Z25	X481 (27.000MHz)		VSS1159
C332, C353, C359, C		CKSRYF104Z25	X601 (16.5MHz)		VSS1160
C609, C622, C631, C	723, C755	CKSRYF104Z25	121		
C758, C761, C762, C	767, C768	CKSRYF104Z25	DVDM AS	SY [VWS15:	31]
C803, C806, C807, C	309-C812	CKSRYF104Z25	SEMICONDUCTO		-
C815, C816, C933, C	936	CKSRYF104Z25	IC801	1110	ADV7172KST
C938, C939		CKSRYF104Z25	IC261, IC302		BA4510F
C112, C125, C126, C	130, C200	CKSRYF105Z10	IC251		BA6664FM
			IC741		HY57V161610DTC
C202, C204, C215, C	217	CKSRYF105Z10	IC101		LA9704W
C221, C222, C226, C		CKSRYF105Z10			
C236, C258, C265, C	299, C310	CKSRYF105Z10	IC201		LC78652W
C319, C323, C328, C		CKSRYF105Z10	IC781		M2V64S40DTP-7
C412, C418, C423, C	428	CKSRYF105Z10	IC351		M56788AFP
			IC751		M65776AFP
C475, C476, C556, C		CKSRYF105Z10			MM1385EN
C607, C608, C610, C		CKSRYF105Z10			
C618, C657, C658, C		CKSRYF105Z10 CKSRYF105Z10	⚠ IC791		MM1561JF
C706-C710, C712-C		CKSRYF105Z10 CKSRYF105Z10	⚠ IC402		MM1565AF
C718-C722, C724-C	132, C/35	CNSHYF105Z10	IC601		PD6345A
0744 0744 0740 0	7.47	CKSRYF105Z10	IC701		PE5286A
C741-C744, C746, C			⚠ IC403		PQ025EZ01ZP
C753, C754, C756, C		CKSRYF105Z10			
C759, C760, C763-C		CKSRYF105Z10	IC481		SM8707HV
C769-C780, C782-C	790, 0792	CKSRYF105Z10 CKSRYF105Z10	IC931		TC74HC4053AFT
C797, C956, C957		CKSHYF105Z10	IC786		TC74VHC541FT
ESISTORS			IC303, IC304		TC7SZU04F
			IC603		VYW2016

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5	6	-	7	-	8	
Mark No. Description	Part No.	Mark No	Des	cription	Part No.	
Q932-Q940	2SA1576A	C105, C	C118, C122, C2	53, C256	CKSRYF104Z25	
Q241	DTC114EUA	C332, 0	C353, C359, C3	65, C366	CKSRYF104Z25	
Q101, Q102, Q106	HN1A01F	C609, C	C622, C631, C7	23, C755	CKSRYF104Z25	
Q103, Q104	HN1B04FU	C758, C	C761, C762, C76	67, C768	CKSRYF104Z25	
Q931	RN1911	C803, C	C806, C807, C8	09-C812	CKSRYF104Z25	
Q601, Q941	RN4982		C816, C933, C93	36	CKSRYF104Z25	
D302, D303	KV1470	C938, C			CKSRYF104Z25	
D401, D402	RB051L-40		C125, C126, C13		CKSRYF105Z10	
D601	RB501V-40		C204, C215, C2 C222, C226, C2		CKSRYF105Z10 CKSRYF105Z10	
COILS AND FILTERS						
L304	LCYA1R2J2520		C265, C299, C3		CKSRYF105Z10	
L4080, L4090, L4100 CHIP BEADS	VTL1074		C329, C409, C4		CKSRYF105Z10	
L4110, L4120 CHIP BEADS	VTL1074		C428, C475, C47		CKSRYF105Z10	
L4130, L4820, L4880 CHIP BEADS	VTL1074		C605, C607, C6		CKSRYF105Z10	
L4910, L4920 CHIP BEADS	VTL1074	C613-C	C616, C618, C6	57, C658	CKSRYF105Z10	
L8020 CHIP BEADS	VTL1074		C706-C710, C7		CKSRYF105Z10	
L4830, L4890, L4900 CHIP BEADS	VTL1081		C722, C724-C7		CKSRYF105Z10	
L4800, L481 CHIP BEADS	VTL1084		C744, C746, C74		CKSRYF105Z10	
			C754, C756, C75		CKSRYF105Z10	
CAPACITORS		C759, C	C760, C763-C76	65	CKSRYF105Z10	
C480, C481, C662	CCSRCH100D50				01/07)/5/057/0	
C121, C950, C952-C955	CCSRCH101J50		C780, C782-C79	90, 0792	CKSRYF105Z10	
C314, C474, C798	CCSRCH150J50	C797, C	C956, C957		CKSRYF105Z10	
C100, C133	CCSRCH151J50					
C120	CCSRCH181J50	RESIST	DRS			
		R631, F	7713		RAB4C103J	
C484, C485, C487, C491	CCSRCH220J50	R111			RAB4C220J	
C134, C324, C391, C392	CCSRCH331J50	R113, F	R704, R705		RAB4C470J	
C945, C946	CCSRCH331J50	R138			RS1/10S0R0J	
C109	CCSRCH391J50	R341			RS1/10S101J	
C297	CCSRCH470J50					
		R141-F			RS1/10S220J	
C241	CCSRCH560J50	R973, F			RS1/16S1000F	
C107, C360	CCSRCH681J50		R369, R373, R37	75	RS1/16S1003F	
C488, C490	CCSRCH820J50	R123			RS1/16S1202F	
C117, C123, C128, C201, C254	CEV101M16	R936, F	R944, R950, R96	66	RS1/16S1500F	
C368, C369, C403, C405, C411	CEV101M16					
		R358, F	R361		RS1/16S1503F	- 1
C413, C414, C419, C422, C801	CEV101M16	R755			RS1/16S1801F	
C103	CEV220M16	R956, F	R971, R979		RS1/16S2200F	
C119, C205, C326, C421, C424	CEV221M4	R754			RS1/16S3001F	
C470, C472, C601, C623	CEV221M4	R751			RS1/16S3301F	
C701, C702, C711, C745	CEV221M4					
		R132			RS1/16S4702F	
C751, C752, C766, C781, C791	CEV221M4	R810, F	R817		RS1/16S6800F	
C793	CEV221M4	R357, F	R362, R363, R36	68, R372	RS1/16S6802F	
C101	CEV470M6R3	R374			RS1/16S6802F	
C116, C127, C223, C224, C264	CKSQYB105K10	R257 (F	R=1.0)		VCN1127	
C312, C406, C407, C415, C416	CKSQYB105K10					
			R259 (R=2.2)		VCN1128	
C477, C794, C795	CKSQYB105K10	Other F	Resistors		RS1/16S###J	- 1
C216, C313, C351, C427, C606	CKSRYB102K50					
C617, C621, C703, C748	CKSRYB102K50	OTHERS	S			
C817, C818, C951	CKSRYB102K50	CN401	PH CONNECT	TER	S13B-PH-SM3	
C110, C113, C203, C220, C225	CKSRYB103K50		CONNECTOR		S5B-PH-SM3	
0.10, 0.10, 0.00, 0.00, 0.00			FLEXIBLE CABI		VDA1681	
C261, C320-C322, C330, C404	CKSRYB103K50		4P CONNECT		VKN1409	
C426, C619	CKSRYB103K50		12P CONNEC		VKN1416	
C108, C111, C114, C115	CKSRYB104K16					
C212, C213, C227, C231	CKSRYB104K16	CN402	17P CONNEC	TOR	VKN1421	
C248-C251, C255, C263, C315	CKSRYB104K16		21P CONNEC		VKN1425	
0201, 0200, 0200, 0010			30P CONNEC		VKN1434	
C317	CKSRYB104K16		26P CONNEC		VKN1790	
C106	CKSRYB152K50		N2 EARTH ME		VNF1109	
C208	CKSRYB222K50	,				
C266	CKSRYB224K10	X481 (3	27.000MHz)		VSS1159	
C206, C214, C242, C357	CKSRYB472K50		16.5MHz)		VSS1160	
0200, 0214, 0242, 0007						

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1 -	2	3	4
Mark No. Description	Part No.	Mark No. Descr R410, R420, R510, R520	ription Part No. RN1/16SE2201D
JACB ASSY [VWV19	121	R332, R333, R342, R343	
SEMICONDUCTORS		R411, R418, R421, R427,	
	DSD1702EG	R518, R521, R527	RN1/16SE8201D
IC401, IC501		R1101	RS1/10S0R0J
IC701	LA73054	R752, R757-R761	RS1/16S75R0F
IC304, IC305, IC402, IC502	NJM2068M	n/32, n/3/-n/01	1131/103/3/101
IC302, IC303	NJM4565M	Other Resistors	RS1/16S###J
<u></u> IC102	NJM78M05FA	Other Hesistors	HS1/16S###J
<u></u> \(\text{LC101}	NJM78M08FA	OTHERS	
	PCM1738EG-3	CN704 SOCKET	AKP7050
IC301	PQ05RD11	JA602 OPT. LINK OUT	GP1FA502TZ
<u>1</u> IC702			
IC201	TC74VHC157F	JA801, JA802 JACK	RKN1004 VEF1040
IC202	TC7SH08F	PCB BINDER	
		JA302 JACK	VKB1125
IC203	TC7SHU04F		
Q312, Q322, Q432, Q532, Q534	2SA1037K	JA301 JACK	VKB1133
Q601, Q801, Q802	2SC2412K	JA702 JACK	VKB1151
Q350, Q351, Q360, Q361, Q410	2SD2114K	JA701 JACK	VKB1156
Q420, Q510, Q520	2SD2114K	JA601 JACK	VKB1160
		CN101 21P CONNECTO	OR VKN1252
Q201, Q310, Q311, Q320, Q321	DTC114YK		
Q430, Q431, Q530, Q531, Q533	DTC114YK	CN701 30P CONNECTO	PR VKN1261
D701-D712, D801, D802	1SS355	CN801 7P CONNECTOR	
D380	UDZS6.2B	CN102 19P CONNECTO	
0000	0000000	KN101, KN102 EARTH N	
COILS AND FILTERS			
L701, L702 CHIP BEADS	VTL1089		
L/UI, L/UZ CHIP BEADS	VIL1069	JACB ASSY [\	/WV19131
A DA CITODO		SEMICONDUCTORS	-
CAPACITORS		IC701	LA73054
C307, C406, C506	CCSRCH331J50		
C115, C116, C118-C120, C801	CCSRCH470J50	IC304, IC305, IC402, IC50	
C702, C721	CEAT101M16	IC302, IC303	NJM4565M
C701, C742, C753, C761	CEAT102M6R3	⚠ IC102	NJM78M05FA
C350, C360, C414, C424	CEAT470M16	⚠ IC101	NJM78M08FA
		IC301	PCM1738EG-3
C110, C725, C762, C763	CEAT471M6R3		PCM1742KE
C101, C103, C107, C314, C324	CEJQ101M16	IC403, IC503	PQ05RD11
C338, C372, C380, C401, C410	CEJQ101M16	⚠ IC702	
C416, C420, C501, C510, C516	CEJQ101M16	IC203	TC7SHU04F
C520, C605	CEJQ101M16	Q312, Q322, Q371, Q432,	, Q532 2SA1037K
		0001	004400716
C604	CEJQ1R0M50	Q534	2SA1037K
C109, C201, C301, C303, C402	CEJQ331M6R3	Q601, Q801, Q802	2SC2412K
C502	CEJQ331M6R3	Q350, Q351, Q360, Q361,	
C514, C524	CEJQ470M16	Q420, Q510, Q520	2SD2114K
C305, C306, C405, C505	CEJQ470M6R3	Q310, Q311, Q320, Q321,	, Q370 DTC114YK
C411, C421, C511, C521	CKSRYB272K50	Q430, Q431, Q530, Q531,	
C332, C333, C342, C343	CKSRYB472K50	D701-D712, D801, D802	
C102, C104, C106, C108, C112	CKSRYF104Z25	D380	UDZS6.2B
C117, C302, C304, C315, C325	CKSRYF104Z25		
C339, C373, C381, C403, C404	CKSRYF104Z25	COILS AND FILTERS	
0000, 0010, 0001, 0100, 0101		L701, L702 CHIP BEADS	S VTL1089
C407, C413, C417, C423	CKSRYF104Z25	,	
C503, C504, C507, C513, C517	CKSRYF104Z25	CAPACITORS	
C523, C601, C606, C703, C704	CKSRYF104Z25		CCSRCH331J50
	CKSRYF104Z25	C307, C406, C506	
C711-C716, C754, C803, C805	CKSRYF104Z25 CKSRYF105Z10	C115, C116, C118-C120,	
C111, C114, C202, C204	CROMIT 105Z10	C702, C721	CEAT101M16
	01/055/5105710	C701, C742, C753, C761	CEAT102M6R3
C722-C724, C726	CKSRYF105Z10	C350, C360, C414, C424	CEAT470M16
C310, C311, C320, C321	CQMBA222J50		
C334, C336, C344, C346	CQMBA471J50	C110, C725, C762, C763	CEAT471M6R3
C412, C422, C512, C522 (1608CH3	30P) VCH1226	C101, C103, C107, C314,	C324 CEJQ101M16
		C338, C372, C374, C380,	
RESISTORS		C410, C416, C420, C501,	
B330 B331 B334 B335	RN1/16SE1001D	C516, C520, C605	CEJQ101M16

RN1/16SE1001D RN1/16SE1001D

RN1/16SE1602D

RN1/16SE2000D

C516, C520, C605

C109, C301, C303, C402, C502

C604

CEJQ101M16

CEJQ1R0M50 CEJQ331M6R3

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RESISTORS R330, R331, R334, R335 R340, R341, R344, R345

R310, R311, R320, R321

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R301

Mark No.	Description	Part No.	Mark No.	Description	Part No.
C514, C524		CEJQ470M16	C903		CCSRCH100D50
C305, C306, C	MOE CEDE	CEJQ470M6R3	C950		CCSRCH102J50
	MUD, C000	CKSRYB104K16	C931		CCSRCH470J50
C370, C371		CUSHIDIONIO		C839, C840, C844	CEJQ221M6R3
	E44 0504	OKODABOZOKEO		C911, C922, C926	CEJQ221M6R3
C411, C421, C		CKSRYB272K50	C901, C909,	0311, 0322, 0320	CLUCKE INIOI IO
C332, C333, C		CKSRYB472K50	0044 0054		CEJQ221M6R3
	106, C108, C112	CKSRYF104Z25	C944, C951	0010	
C117, C302, C	304, C315, C325	CKSRYF104Z25	C828, C908,	Cale	CKSRYB103K50
C339, C373, C	381, C403, C404	CKSRYF104Z25	C827		CKSRYB473K25
			C807-C812,	C815, C816	CKSRYF105Z10
C407, C413, C	417 C423	CKSRYF104Z25	C819-C824,	C826, C830-C837	CKSRYF105Z10
C503 C504 C	507, C513, C517	CKSRYF104Z25			
	606, C703, C704	CKSRYF104Z25	C841, C843.	C902, C905-C907	CKSRYF105Z10
		CKSRYF104Z25	C912-C915,		CKSRYF105Z10
	754, C803, C805	CKSRYF105Z10		C927-C930, C934	CKSRYF105Z10
C111, C114, C	722-C724, C726	CKSHTF 1002 10	C937, C938,		CKSRYF105Z10
		0.0140.4000.000		C955, C956, C991	CKSRYF105Z10
C310, C311, C		CQMBA222J50	C945-C947,	C955, C956, C991	CK5H1F105210
C334, C336, C		CQMBA471J50			
C412, C422, C	512, C522 (1608CH3	30P) VCH1226	RESISTORS		
			All Resistors		RS1/16S###J
RESISTORS					
R330, R331, F	1334 P335	RN1/16SE1001D	OTHERS		
R340, R341, F		RN1/16SE1001D	PCB BINDI	=n	VEF1040
	1344, 11343	RN1/16SE1602D			VKN1460
R301	nee Book		CN801 20P	CONNECTOR	
R310, R311, F		RN1/16SE2000D		CONNECTOR	VKN1775
R410, R420, F	8510, R520	RN1/16SE2201D	CN901 FFC	CONNECTOR	VKN1794
R332, R333, F	342, R343	RN1/16SE3001D	FI 107	A COV DIMOSS	E 41
R370, R371		RN1/16SE3902D	FLKY	ASSY [VWG23	04]
R372, R411, F	R418, R421, R427	RN1/16SE8201D	SEMICONDU	ICTORS	
R511, R518, F	8521 B527	RN1/16SE8201D	IC101		PE5314B
R1101	,	RS1/10S0R0J			PST3228
niivi			IC102		
R752, R757-F	2761	RS1/16S75R0F	Q103, Q105		2SA1602A
			Q104		2SC2412K
Other Resistor	'S	RS1/16S###J			
			SWITCHES	AND RELAYS	
OTHERS			S101-S106		ASG7013
CN704 SOCI	KET	AKP7008	2.2. 5.00		
JA602 OPT. I		GP1FA502TZ	CAPACITOR	9	
JA801, JA802		RKN1004		<u>.</u>	0000001400460
PCB BINDE		VEF1040	C107, C108		CCSRCH102J50
JA701 JACK		VKB1122	C104		CEAL470M6R3
JA701 JACK			C100		CEJQ101M6R3
14000 14016		VKB1126	C116		CKSRYF104Z50
JA302 JACK		VKB1126 VKB1132	C102, C105,	C110, C113, C115	CKSRYF105Z10
JA301 JACK					
JA702 JACK		VKB1150	RESISTORS		
JA601 JACK		VKB1159	All Resistors		RS1/16S###J
CN101 21P	CONNECTOR	VKN1252	All riesistors		110 II 100 II III
			OTHERS		
CN701 30P	CONNECTOR	VKN1261	OTHERS		
CN801 7P C		VKN1267		NNECTOR 4P	04P-FJ
	2 EARTH METAL FIT	TING VNF1084	IC103 REM	IOTE RECEIVER UNIT	SPS-452L-H
14101,1410			V101 FLTU	JBE	VAW1073
			SPACER		VEC2220
SACDE	3 ASSY			CONNECTOR	VKN1277
			0,,,01		
SEMICONDU	CIORS		HOLDER		VNF1122
⚠ IC906		BA25BC0FP		la)	VSS1142
IC901		CXD2753R	X101 (5MH	12)	V 30 1 142
IC902		HY57V161610DTC-8			
/↑ IC808		MM1561JF	3 EL VV	ASSY [VWG23	761
IC904		TC7SH00FU			70]
10004			SEMICOND	UCTORS	
10004		TC7SH02F	IC101		PE5314B
IC991			IC102		PST3228
IC806		TC7SH04FU			2SA1602A
IC905		TC7WH74FU	Q103, Q105		
IC801		XCA56367PV150	Q104		2SC2412K
COILS AND	FILTERS		SWITCHES	AND RELAYS	
L801		LCYA1R0J2520	S101-S106		ASG7013

CAPACITORS

Mark No. Description

C107, C108 C104 C100

C116

C102, C105, C110, C113, C115

RESISTORS All Resistors

RS1/16S###J

Part No. CCSRCH102J50

CEAL470M6R3

CEJQ101M6R3

CKSRYF104Z50

CKSRYF105Z10

04P-FJ

SPS-452L-H

VAW1073

VEC2220

VKN1277 VNF1122

VSS1142

AEK7012

AEK7063

AEK7066 AEK7067 REK1077

OTHERS CN102 CONNECTOR 4P

IC103 REMOTE RECEIVER UNIT V101 FLTUBE SPACER

CN101 17P CONNECTOR

HOLDER X101 (5MHz)

KEYB ASSY

SEMICONDUCTORS D203, D204 SLR-343VC(NPQ)

SWITCHES AND RELAYS

S201-S203 ASG7013

RESISTORS All Resistors

RS1/16S###J

OTHERS CN201 CONNECTOR 4P 04R-FJ

C POWER SUPPLY UNIT OTHERS

♠ P103 PROTECTOR(1.6A) A P101 PROTECTOR(800mA) ↑ P101 PHOTECTOR(800m/ ↑ P102 PROTECTOR(1.6A) ↑ P104 PROTECTOR(2A) ↑ FU1 FUSE(1.6A)

6. ADJUSTMENT

6.1 ADJUSTMENT ITEMS AND LOCATION

■ Adjustment Items

[Mechanism Part]

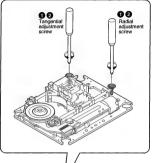
- 1 Tangential and Radial Height Coarse Adjustment
- 2 DVD Jitter Adjustment
- 3 Initialize the Focus Sweep Setting

[Electrical Part]

Electrical adjustments are not required.

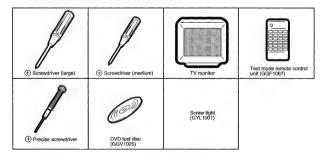
Adjustment Points (Mechanism Part)

Cautions: After adjustment, adjustment screw locks with the Screw tight.





6.2 JIGS AND MEASURING INSTRUMENTS



6.3 NECESSARY ADJUSTMENT POINTS

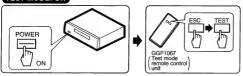
When Adjustment Points ■ Exchange Parts of Mechanism Assy Mechanical point After adjustment, screw locks with the Screw tight. Exchange the Pickup 0. 0. 0 Electric point Exchange the Traverse Mechanism point Mechanical point * After adjustment, screw locks Exchange the Spindle Motor with the Screw tight. ■ Exchange PCB Assy Exchange PC Board LOAB, DVDM ASSY Purpose: To set the sweep which was correct with the individual Traverse mechanism. Be sure to perform the following step finally when replaced Pickup, Traverse Mechanism and Spindle Motor. GGF1067 Test mode remote control (It is necessary when performed adjustment procedure @.)

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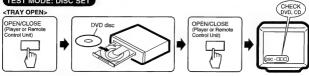
6.4 TEST MODE

TEST MODE: ON



TEST MODE: DISC SET

<TRAY OPEN>



7

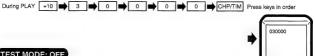
TEST MODE: PLAY





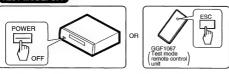
< When playback with the target address of disc (DVD)>

For example, when playback with # 30000



TEST MODE: OFF

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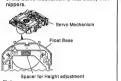
6.5 MECHANISM ADJUSTMENT



1 Tangential and Radial Height Coarse Adjustment

START

· Remove the servo mechanism. . Remove a Spacer for height adjustment attached to the back side (shaded area) of the Servo Mechanism (Float Base) with



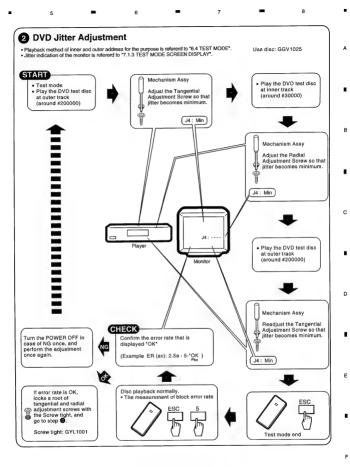
Note: Turn the Short switch to Short side when removing the Pickup Flexible Cable. (Refer to "7.1.9 DISASSEBLY".)

Because there is not a Spacer for height adjustment in adjustment after the second time, will keep it at need. (This parts is Traverse mechanism exclusive use of a model for 2001 years)



Put a spacer between a Tangential (or Radial) adjustment screw and Mechanism Base and turn each screw to adjust the height. (Refer to "6.1 ADJUSTMENT ITEMS AND LOCATION".)





DV-45A

Purpose: To set the sweep which was correct with the individual Traverse mechanism.











Note: Be sure to perform this step when replaced the Pickup or Traverse mechanism.

7. GENERAL INFORMATION

7.1 DIAGNOSIS

7.1.1 ID NUMBER AND ID DATA SETTTING

Entering the ID Number and ID Data for Players with DVD-Audio and DVD-RW Compatibility

It is necessary with a player with DVD-audio and DVD-RW compatibility to set an individual number (ID number) and ID data. If the number and data are not set correctly with the following procedure, operations in the future may not be guaranteed. You will find the ID number to be set on the yellow label on the rear panel.

Important: If no yellow label is found on the rear panel, write down the specified ID number by checking it according to "How to confirm the ID number" shown below.

■ The Input is Necessary When:

- . Downloading FLASH-ROM is finished. (The latest version must be downloaded when a repair is made.)
- . "No ID Number" is displayed on the screen or FL display immediately after the power is turned on or in Stop mode.
- . If "No ID DATA" is displayed, the ID data must be entered.

Note:

Be sure to enter the ID number in Stop mode.

Use the service remote control (GGF1067) for operations. Only opening/closing of the tray are performed from the player. Use Disc No.: GGV1084

How to Input the ID Number and ID Data

- ① To enter the input mode, press ESC + STEREO in a status with no ID number set, such as after FLASH-ROM downloading.
- ② As number input is enabled when the unit enters the input mode, input the 9-digit ID number.



3 After inputting the number, press SEARCH to register the ID number.



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(4) When the ID number has been registered, the unit enters the ID data input mode. (The FL display indicates "NO ID DATA.") In this condition, place the ID data disc on the tray and close the tray using the CLOSE key "w/e" on the player.



(§) While the data are being read, the message shown in the figure at left is displayed on the screen. (The FL display indicates "RD ID DATA.")



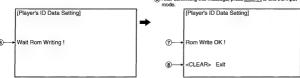
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(6) When the ID data have been read, the data are written to the FLASH-ROM.

(The FL display indicates "WR ID DATA.")

When the ID data have been written to the FLASH-ROM, the message "Rom Write OK" is displayed on the screen. (The FL display indicates "ID DATA OK.")

After confirming this message, press CLEAR to exit the input



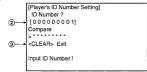
How to Confirm the ID Number

- Press ESC + STEREO with an ID number set, and the unit enters the ID number confirmation mode.
 - The set ID number is displayed on the screen (and on the FL display), permitting you to confirm it.
- 3 To exit this mode, press CLEAR.

В

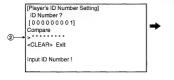
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How to Clear the ID Number

- Press <u>ESC</u>+<u>STEREO</u> with an ID number set, and the unit enters the ID number confirmation mode.
- (2) Input the same number as the ID number you have set.



③ After inputting the number, press[STOP].
Only when the entered number matches the set ID number, the ID number is cleared and the unit exits this mode.
If the numbers do not match, you must return to step 2.
(STOP) is not accepted until 9 digits are entered.)

This unit can confirm the laser diode current value (DVD: 650nm, CD: 780nm) of pickup on the Test Mode screen. (Press the $|\overline{\text{ESC}}| \rightarrow |\overline{\text{TEST}}|$ keys in order on the test mode remote control unit (GGF1067) to enter the test mode.)

It's effective in case of the following condition.

Symptom

- · Indicates "No Disc" in FL display.
- Player does not playback, etc..

Procedure of Self-Diagnosis

- 1 Enter the Test mode.
- ② When diagnosing the 650nm laser diode:
 - Press the TEST → 1 keys in order, and turn on the laser diode (It light-up for nine seconds.).
 - When diagnosing the 780nm laser diode:

 Press the TEST \rightarrow 4 keys in order, and turn on the laser diode (It light-up for nine seconds.).
- 3 Confirm the indicated value of the laser diode current (LDI). (Refer to following figure.)
- ⊕ When indicated value is more than 100, pickup is defective. → Replacement is necessary Replace the Traverse Mechanism Assy or Pickup.

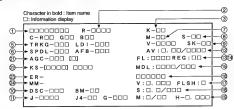
Note: When a DVD disc or a CD disc is played in the test mode, this function is effective.

Character in bold : Item name

	LI: Information display				
Laser diode current value —	C-R:: G:: B:: TRKG :: LDI-::: LDI-::: SPDL-:: AFB-::: LDI-:: AGC-:: [:]	K-00 M-00 S-00 V-0000 SK-00 AV:0.00/0000 FL:0000 REG:00			
	KS-[0000] 0000	MDL: 0000/000			
	ER- MM- DSC BM J J4 G	V:0.000 FLSH:0 S:0.0/000 M:0/00 H-0.000			

7.1.3 TEST MODE SCREEN DISPLAY

■ Display Specification of the Test Mode



1 Address indication

С

The address being traced is displayed in number.

(as for the DVD, indication of decimal number is possible.)

DVD: ID indication (hexadecimal number, 8 digits)

[*********]

[*******]
CD : A-TIME (min. sec.) [0 0 0 0 ****]

- ② Code indication of remote control unit [R * * * *] In case of double code, display a 2nd code.
- 3 Main unit keycode indication [K * *]
- 4 Background color indication [C R** G** B**]
- (1) Tracking status [TRKG * * *]

 Tracking on : [ON]

 Tracking off : [OFF]
 (2) Laser diode current value [LDI * * *]
 - .

(a) (1) Spindle status [SPDL - * * *]
Spindle accelerator and brake, free-running
FG servo
Rough, velocity phase servo
(2) AFB status [AFB - * *]
ON
ON
ON
OFF
(DOP)
(DOF)

Mechanism (loading) position value [M - * *] Unknown : [01] or [41]

Open state : [04] Close state : [08] During opening : [12] During closing : [22]

® Slider position [S - * * * *]

CD TOC area : [IN] CD active area : [CD] Output video system [V - * * * *]

NTSC system : [NTSC]
PAL system : [PAL]
Automatic setting : [AUTO]
Scart terminal output [SK - * *]

(Display only the WY model which can do the output setting of scart terminal.)

VIDEO : [00] S-VIDEO : [01] RGB : [02]

3

(i) (1) Disc sensing [DSC - * * *] The type of discs loaded is displayed.

[DVD], [CD], [VCD], []
(2) CD 1/3 beam switch [BM - * *]

① Jitter value [J - * * * *]

Make the jitter four times, and renew it in every 0.5 second. [J4-**]

- @ Version of the AV-1 chip / version of firmware
- [AV: **/********]

 ③ Version of the FL controller [FL: ****]
- Region setting of the player [REG: *] Setting value: [1] to [6]
- (§) Destination setting of the FL controller [MDL: * * * * / * * *]

Four characters in the front represent the type of model.

Three characters in the back represent the destination code.

J: /J, K: /KU, /KC, /KU/KC, R: /RAM/RL/RD, LB: /LB,
WY: /WY

- (6) Part number of the flash ROM and system controller [* * * * * * / * * * * * *]
- ⑦ Version of the flash ROM [V: *. * * *] Flash ROM size [FLSH = *]
 - (8 Revision of the system controller [S: * . * / * * *]

(9 (1) Revision of the DVD mechanism controller

[M: */**]

(2) Part number of the GUI-ROM (OEM model)

[GUI: * * *]
(3) HOST conversion [HOST: * * *]

② AGC setting [AGC - * * * [*]]

AGC setting [AGC - * * * [*]]
AGC on : [AGC-ON]
AGC off : [AGC-OFF]

[1]: RFAGC on [0]: RFAGC off

2 FTS servo IC information

DSP coefficient indication [KS - [****] ****]
Displays the address (four digits) of the specified coefficient and the setting value (four digits) with [TEST] and [9] keys.

3 Error rate indication

① C1 error value of CD [ER - C1 * * * *]

② C1 error value of DVD [ER - * * * * * * * *]

② Internal operation mode of mechanism controller

[MM - * * : * *]

5

Internal mechanism mode (2 digits) and internal mechanism step (2 digits) of the mechanism controller

7.1.4 SELF-DIAGNOSIS FUNCTION

When enter the service mode, self diagnosis mode operates with the "ESC"+"CHP/TIM" keys automatically.

2

Mechanism Error History (past eight times of error is displayed) Two columns of the beginning display the error status for mechanism controller.

(the details of error contents refer to "7.1.4 Error Display".)

Eight columns of the back display the count UP value (turned count up every 20msec) from the power-up.

Example) 32h = 1 sec, BB8h = 1 min, 2BF20h = 1 hour

In addition, when there was error after power-up immediately (till initial setting is completed), turn the most significant bit to ON.

2 Check Item Display of Self Diagnosis Function

a) AV1 Host Bus check (possible the check only during stop) (Read & Write process of an internal specific register)

```
AV_1 : OK
                          ⇒ not yet check
      : HOST BUS NG
                          ⇒ HOST bus NG
```

b) Bus check between AV1 SDRAM (possible the check only during stop) (Read & Write process to the SDRAM)

AV_2 : OK

1

⇒ not yet check

c) DMA transfer port check from F.E. to AV1 (during stop, possible the check only in DVD or NO DISC) (writing from F.E to SDRAM and reading of SDRAM)

AV 3 : OK ⇒ not yet check

· FE-AV1 DMA NG ⇒ Bus NG between F.E and SDRAM installed outside of AV1 d) Video encoder (ADV****) check (Read of the specific register)

: AV1-SDRAM BUS NG => Bus NG between AV1 and SDRAM

: OK NG ADV,

⇒ ADV register reading NG ⇒ ADV communication NG of FR to video encoder

: NG > ADV, : NG > PRO ⇒ Communication NG from EBY to progressive decoder e) DSP check (Read of the specific register)

DSP : OK : NG ⇒ DASP NG f) SACD check (Read of the specific register)

SACD : OK ⇒ SACD NG · NG

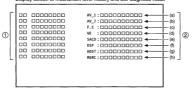
g) 1394 relation HOST controller check HOST : OK

: NG ⇒ HOST controller NG h) 1394 relation Mercury CHIP check MERC : OK

⇒ Mercury CHIP NG : NG

Display the mechanism error history and self diagnosis result by pressing the "CHP / TIM" key once again. Afterwards press the "CHP / TIM" key with toggle and change the display.

Display screen of mechanism error history and self diagnosis result



D

7.1.5 FUNCTION SPECIFICATION OF THE SERVICE MODE

FL indication of EDC / ID error (short cut function)

Indicate it in FL with the "ESC"+"CX" keys (LD remote control unit). Indication is released with the "ESC" key during display.

El indication contents

00/00/01 *

Indicate number of the location that caused EDC and ID errors

Retry number of times at having caused ID error (error is indicated only in the occurring moment) Retry number of times of the latest ID error in the ST system

Retry number of times at having caused EDC error (error is indicated only in the occurring moment) Retry number of times of the latest EDC error in the ST system

* Mark: When even once causes AV1 error, lights.

· Screen display of the service mode Indicate to the screen with the "ESC"+"CHP/TIM" keys.

Release the indication with the "ESC" key. Indication contents

ID Address

② DVD in playback: Error rate regular indication and exponent indication

CD/VCD in playback indicates the number of correct frame of C1 error /5 seconds.

③ Self diagnosis Indication Indicate the self diagnosis result whether the F.E is normal.

: During FE checks Salf Check Self Check OK : Abnormality is not found in F.E.

Self Check Error : Abnormality is found in F.E. Indicate the mechanism error history and self diagnosis result

by pressing the "CHP / TIM" key once again.

Afterwards press the "CHP / TIM" key with toggle and change the display.

Indication of the mechanism error history and self diagnosis result refer to "7.1.1 self diagnosis function".

Error information indication of the AV decoder

When a retry occurred in reading from the disc, a history indicates the occurrence location and the occurrence reason. History is indicated to past seven times

Eight columns of the beginning show the physical address which occurred of retry.

As for four columns of next, bitmap indicates EDC status. LSB shows the first sector during a block and MSB shows a last sector.

Following field indicates the retry number of times.

One digit in front of " / " shows number of times of the retry by EDC Error which occurred in the same block in succession.

Indication contents

One digit after " / " shows number of times of the retry by ID Check Error which occurred in the same block in succession. * of last one digit shows the EDC Check NG Count Over. "#" shows the ID Check NG Count Over.
When " " and " # " are not indicated, show that data were

rightly readable by retry process.

(b) Indicate the error information that detected with the Audio/Video Decoder. When error occurred, a history indicates the occurrence time and the occurrence reason. History is indicated

to past seven times. Field in front of ":" indicates the error information of Audio/Video

Decoder. (Indication information is different from Fujitsu Decoder with

Mitsubishi Decoder) 02 model is 656 series and 757 series is Mitsubishi model.

 Specification for the Audio/Video Decoder (M65773FP) model of Mitsubishi

bit7: VLD Fatal Error detection

bit6: VLD Not Fatal Error detection

bit5: Number of Macro Block mismatch

00:0000000

bit4: Decode error

bit3: VLD Sequence Layer Fatal Error detection bit2: VLD Picture Layer Fatal Error detection

bit1: VLD Slice Laver Fatal Error detection

bit0: Start-up Sequence Time-out Error detection Following field in ": " indicates a value of STC (System Time Clock) which detected the above Audio/Video Decoder error.

* When often perform the switch of debug screen, an error history will be increased. As for this, CPU power is used for update of OSD drawing, symptoms occur so that control of VBR Buffer is not in time.

00000000 00000000 00:00000000 00:0000000 4 (6) 00:0000000 nn:00000000 nn:00000000 00:00000000 00:0000000

DV-45A

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F

7.1.6 ERROR DISPLAY

Error codes that are displayed on the FL display without using the remote control unit

FL Display	Possible causes	Operation of the unit
AV1 VER	AV-1 chip is not a match with the program of system controller	The sound may not out with the specific audio.
CPU AERR	CPU address error (Hardware is unusual.)	No operation
DMA AERR	DMA address error (Hardware is unusual.)	No operation
FLASH ID	Difference in versions of the internal ROM of the system controller and of the flash ROM, or bus line failure or reverse installation	No operation
FLASH WRP	Write protect error of the flash ROM	No operation
FLASH SIG	Difference in part number of the flash ROM (When the ROM which could't be used was used.)	No operation
FLASH SUM	Check sum error of the flash ROM (It exceeds the regular size.) or reverse installation (Hardware is unusual.)	No operation
FLASH SIZ	Size error of the flash ROM (Use 4 or 8 M-bit.)	No operation
GUI ROM ERROR	Difference in version of GUI ROM and system controller software.	Operate as the OSD model
ILLGAL	The system controller fetched a code other than an operation code (Hardware is unusual.)	No operation
MECHA CPU	Difference in version of the internal ROM of the mechanism controller and of the flash ROM.	No operation
RESERVE	Undefined interrupt (Hardware is unusual.)	No operation
SLOT	Inappropriate slot command issued (Hardware is unusual.)	No operation

Error codes that are displayed on the FL display by using the remote control unit

(Mechanism controller error)
To display: ESC + DISPLAY + DISPLAY: Lost on of the display: At the two digits of center of the FL display
To display the error history: ESC + DISPLAY + One shot; Location of the display: TV screen

FL	Description of Error	Causes if with a DVD	Causes if with a CD	Operation of the Unit
11	Search timeout	Search could not be complete within 7 seconds.	Search could not be complete within 7 seconds, and it could not enter the target area within 7 seconds by VCD scan.	CD : Stops, DVD: Continues operation
12	Search retry error	More beyond the target while the read-in s be completed after 3 retries while the unit be completed after retry when timeout occ	earch was converging. A search could not was tracing 11 tracks. A search could not urs at read-in.	CD: Stops, DVD: Continues operation
19	Tracing timeout while converging	Timeout (10.5 seconds) while tracing at the stage of convergence of a search.		Stop
1B	Index 0 search error		During Track (Index) Search, the search for the beginning of a program could not be completed within 3 seconds (20 seconds in the case of Index Search) after positioning based on the TOC data was completed.	Stop
1C	Embossment plunge error (only a model corresponding to RW)	Plunged into unreadable embossment of DVD-RW player.		In wobble nothing (error distinction): search to address 2E400h In wobble existence: Tray open
22	Timeout of slider inner circumference	Inside switch could not ON within 3 second	Stop	
23	Timeout of slider outer circumference	Inside switch could not OFF within the folloat ATB: 2 seconds, at Backup: 2 or 2.02 s	Stop	
33	No FOK pulse during playback	When the focus was deviated continuously 20 times.		Adjusts focus at the innermost circumference and tries to return to its position where the error was generated (for 3 times), then opens. If the same error persists after one retry, the tray opens. (No FOK pulse)
38	Disc-type- sensing error	Were not able to playback from the disc di PLAY or STOP was not completed by bac Distinguished it from the blank disc in the	Open	

FL	Description of Error	Causes if with a DVD	Causes if with a CD	Operation of the Unit
39	SGC converge timeout	SGC could not converge during detects the peak	Open	
41	Spindle timeout	The unit did not enter Stop mode within 10 seconds of i Disc distinction is not completed even if passes for 10 s	Stop	
48	Spindle FG transition timeout	Did not reach to the rotating speed that ATB was possible for less than 10 seconds. Did not reach aim CAV lock speed (high: 10%, low: 50%) for less than 10 seconds. CAV process passed more than 5 seconds or abnormal speed was detected. Spindle does not lock for less than 3 seconds in the BCA read start or end.	Stops. (FG timeout)	
49	Spindle PLL transition timeout	CAV process passed more than 5 seconds. Abnormal s	speed was detected.	Stops. (*73" is displayed during starting process.)
4A	Spindle lock timeout	Spindle could not lock more than 1.5 seconds before st	art the AFB.	Stops. ("73" is displayed during starting process.)
51	Auto sequence timeout of peak detection	ABUSY did not return within 1 second after the DDTCT (peak detection) command was sent.		Stop
52	Auto sequence timeout of focus jump down	ABUSY did not return within 30 mS after the FJMPD (Focus jump 1 to 0) command was sent.		Open
53	Auto sequence timeout of focus jump up	ABUSY did not return within 30 mS after the FJMPU (Focus jump 0 to 1) command was sent.		Open
54	Auto sequence timeout of play AGC	ABUSY did not return within 50 mS after the GSUMON (play-AGC-measuring) command was sent.		Stop
55	Auto sequence timeout of disc-type- sensing	ABUSY did not return within 2 seconds after the DJSRT (disc-sensing) command was sent.		Stop
56	Auto sequence timeout of ATB2	ABUSY did not return within 1 second after the TBLOFS (Internal ATB after the completion of external ATB) command was sent.		Stop
57	Auto sequence timeout of tracking servo ON	ABUSY did not return within 0.5 sec. after the TSON (tracking servo ON) command was sent.		Stop
58	Auto sequence timeout of ATB1	ABUSY did not return within 0.2 sec. after the TBL (external ATB) command was sent.		Stop
59	Auto sequence timeout of focus gain adjustment	ABUSY did not return within 2 seconds after the FGN (focus gain adjustment) command was sent.		Stop
5A	Auto sequence timeout of tracking gain adjustment	ABUSY did not return within 2 seconds after TGN (tracking gain adjustment) command was sent.		Stop
5B	Auto sequence timeout of offset adjustment	ABUSY did not return within 1 second after the AVE (offset adjustment) command was sent.		Stop
5C	Auto sequence timeout of modulation factor measurement	ABUSY did not return within 200 mS after the ADJMIR (modulation factor measurement) command was sent.		Stop
5D	Auto sequence timeout of auto focus bias	ABUSY did not return within 2 seconds after the AFB (auto focus bias) command was sent.		Stop
5F	Auto sequence already busy	A command could not be sent because ABUSY was low. ABUSY did not return within 200 mS after TLV command was sent.		Stop
62	Pause retry error	Pause mode could not be restored within three retries after it had been released.		Continues operation
71	ID reading check during playback	An ID could not be read for 1 second or more.		Stop
72	Subcode check failure during playback	No frame could be read for 3 seconds or more.		Stop
73	ID can not read during startup	An ID could not be read within 1 second after the AFB tracking on.		Opens (ID readout failure)

73

С

Ε

-

FL	Description of Error	Causes if with a DVD	Causes if with a CD	Operation of the Unit	
74	Subcode check failure during startup		Subcode could not be read within 1 second after the tracking on.	Opens (Subcode readout failure).	
A1	Communication timeout of DSP command	A command could not be issued to DSP because Command Busy (XCBUSY) was in force (XCBUSY = L) for a specified time (about 200 μS).	Open		
A2	Communication timeout for reading DSP coefficient	Command Busy (XCBUSY) was in force for a specified time (about 200 µS) before and after a coefficient read command was issued to DSP, or the address echo-back after command issuance did not match the setup address.	pecified time (about 200 µS) before and after a operation to a command was issued to DSP, or eaddress echo-back after command issuance		
A4	Communication timeout for continuously writing DSP coefficient	Command Busy (XCBUSY) was in force for 200 µS during continuous coefficient writing, or before and after a continuous write command was issued to DSP.		Open	
В1	Timeout error for backup	In the backup sequence, codes could not be read	for fixed time.	Stops	
B2	Retry error for backup	Cannot close tracking even if performs backup fix	ed number of times.	Stops	
ВЗ	Retry error for trace	During tracing, do not restore after the runaway de several times.	etection backup was performed	Stops	
СЗ	Detection of tracking overcurrent	During playback, the overcurrent detection port was continuously.	as at L for 300 ms or more	Stops (the mechanical controller operates independently).	
(C5)	Short-circuit test corresponding error	After the overcurrent detection (C3 error), furthern was at L for 300 mS or more continuously.	Turns off the power instantly (No indication on the FL display and no writing to flash memory)		
F5	Tray being pushed	The tray switch that had been Open mode was for than Open by an external force.	Closes		
F6	Code reading NG		(PH code nothing) When Philips code is not readable during LD starting, and a code was not readable street moved to FWD and REV directions slowly each for five seconds. (PRD) in the CD starting, when a subcode of TOC part was not readable, but the subcode of the program area was readable.	Search, scan and special playback prohibition, Playback as playback CD-R (PRD mode) as it is.	
F8	Loading timeout	Loading or unloading could not be completed within a specified time (about 10 seconds). Though a portable cover is opening, when a close command was issued from the system controller.	Reverses the loading direction. It timeout is repeated upon retry, the unit stops.		
FC	Focus	Focus ON sequence could not be completed more than two seconds. Auto sequence command was finished, actually focus ON was not completed. Focus did not enter even if retried it eight times.		Stops wherever possible then opens (stops in the case of side B).	

Error codes that are displayed on the FL display by using the remote control unit (Device error) To display: ESC + DISPLAY + DISPLAY; Location of the display: At the two digits of left of the FL display

FL	Description of Error	Causes if with a DVD	Causes if with a CD	Operation of the Unit
bit4=1 10 etc.	Mechanism controller RAM check sum error			
bit3≃1 08 etc.	AV1 access error (read, write NG)			No operation or it becomes debugging indication if the power is able to ON.
bit2=1 04 etc.	LSI11 access error			
bit0=1 01 etc.	SRAM access error			

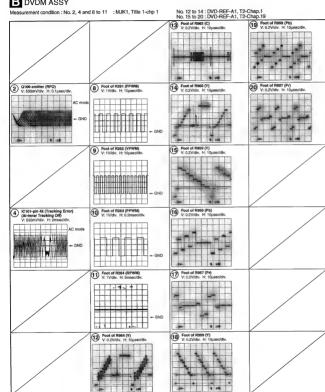
Е

7.1.7 TEST POINTS LOCATION & WAVEFORMS SIDE A SIDE A Υ (14) C (13) 15(18) Y -16(19 Pb -**B** DVDM ASSY V (12) 111111 16MDSP 16MEBY 22/24M 22/24AV1 2 RFO-4)TE 27MAV1 27MDSP 33MSACD **33MEBY1** 36MEBY FPWM(8) VPWM(9) PPWM(10 RPWM(11)

B DVDM ASSY

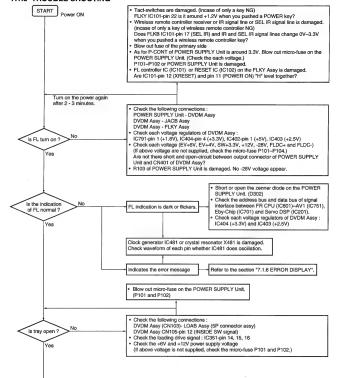
В

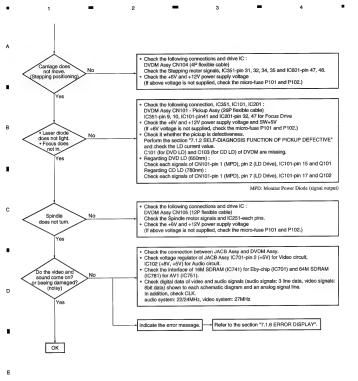
С



3

7.1.8 TROUBLE SHOOTING





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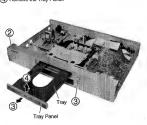
7.1.9 DISASSEMBLY

■ DIAGNOSIS OF PCBs

When diagnosing the unit, be sure to use two extension cables for service (Part No. : GGF1157, GGD1298) and a extension board for service (Part No. : GGF1430).

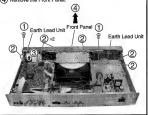
Bonnet and Tray Panel

- 1 Remove the Bonnet (Screws × 6).
- (2) Power ON.
- (3) Tray Open (♠).
- (4) Remove the Tray Panel.



Front Panel

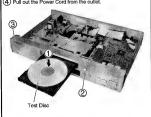
- 1 Remove two Earth Lead Unit (Screws × 2).
- (2) Unhook (× 6).
- Release a Flexible Cable.
- (4) Remove the Front Panel.





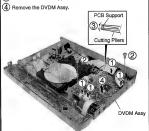
2 Test Disc Set

- 1 Set the Test Disc.
- Tray close (♠). → Clamp the Test Disc.
- 3 Power OFF.
- (4) Pull out the Power Cord from the outlet.



4 DVDM Assv

- 1 Release four Flexible Cables.
- (2) Remove two screws.
- (3) Release the PCB Support.

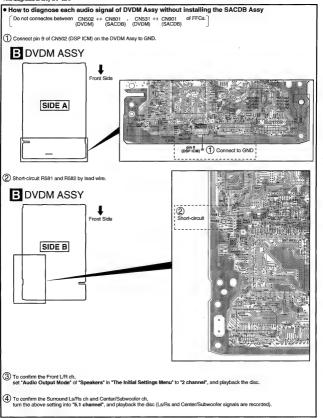


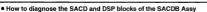


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Diagnosis Method of Audio Block

This diagnosis is only DV-45A.





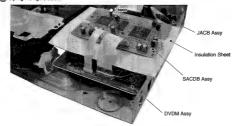
- ① Remove

 Board to Board connector CN102

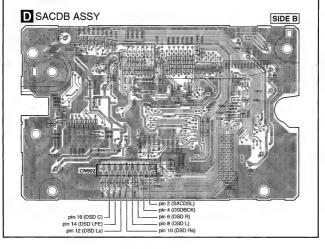
 (JACB)

 CN902 (SACDB)
- 2 styling like figure below.

1



(3) In this case an audio of SACD is not output from the Audio jack. However, observe the signal waveform of CN902 on the SACD8 Assy, and can confirm it. CN902 - pin 2 (SACDSL), pin 4 (DSDBCK), pin 6 (DSD R), pin 8 (DSD L), pin 10 (DSD Rs), pin 12 (DSD Ls), pin 14 (DSD LFE), pin 16 (DSD C).

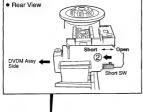


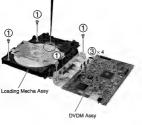
82

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6 Loading Mecha Assy

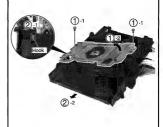
- (1) Remove four Screws.
- Turn the Short SW to short side.
- 3 Remove three Flexible Cables and a Connector.





7 Tray

- 1 Remove the Bridge (Screw ×2).
- 2 Pull out the Tray and remove it while unhooking a hook.



Caution in the Tray Insertion

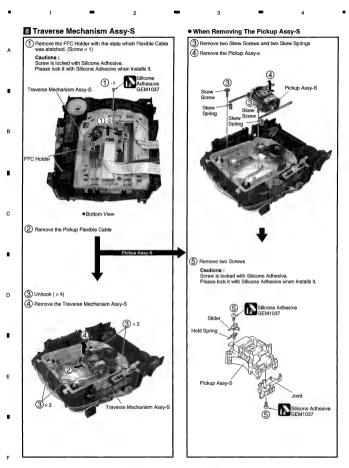
In the Tray insertion, insert it after matching a triangle mark tof the Loading Base and a position of pin of the Drive Cam.

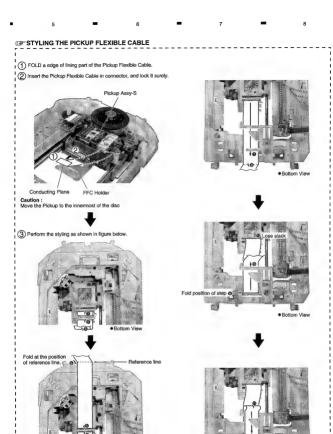


Drive Cam









Bottom View

Bottom View

7.2 IC

• The information shown in the list is basic information and may not correspond exactly to that shown in the schematic diagrams.

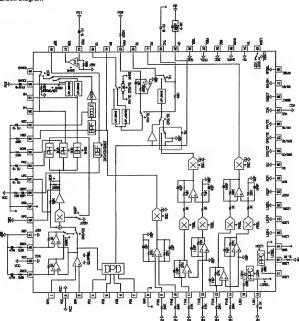
• List of IC

LA9704W, LC78652W, BA6664FM, SM8707HV, PD6345A, M65776AFP, AD7172KST, PCM1738EG-3, DSD1702EG, LA73054, CXD2753R, PE5314B, PE5286A, PCM1742KE

■ LA9704W (DVDM ASSY : IC101)

• RF IC

Block Diagram



No.	Pin name	Pin Functions
- 1	RFN	RF- input
2	vcc	Power supply terminal (for DPD)
3	RFP	RF+ nput
4	PD1	
5	PD2	District street in the
6	PD3	Pickup signal input
7	PD4	
8	GND	Ground (for DPD)
9	PIN1	
10	PIN2	
11	TIN1	
12	TIN2	Pickup signal input
13	FIN1	
14	FIN2	
15	LDD1	APC1 output
16	LDS1	APC1 monitor input
17	LDD2	APC2 output
18	LDS2	APC2 monitor input
19	GND	Ground (Servo system)
20	LDTH	APC1 threshold change (H: VCC-1.5V, L: 180mV)
21	LDON	Laser ON terminal (H: ON)
22	LDSEL	APC change terminal (H: APC1)
23	AGOF	RFAGC off terminal
24	BCA	PH electric discharge coefficient change (H: BCA mode)
25	GU	RF, Servo signal gain up terminal (H: Gain up)
26	DVD/CD	RF- equalizer band change terminal (H: DVD)
27	DPD/TE	TE output change terminal (H: DPD)
28	PP/TE	TS output change terminal (H: PP)
29	VCC	Power supply terminal (Servo system)
30	EQSCT	EQ change for CD (H: 62 pin choice)
31	WO/BH	BHMIX output change terminal (H: WOBLE)
32	RFSEL	RF amplifier gain change (H: 6dB up)
33	LDDM	LDD monitor terminal
34	TH	Tracking hold (H: hold)
35	XHTR	Tracking, Bottom band change (L: High bandwidth)
36	SGC	Servo gain control terminal (FE, PP, TE)
37	FEBL	FE balance adjustment terminal
38	TEBL	TE balance adjustment terminal
39	CP	Resistance for charge pump gain setting, a condenser connection terminal
40	THC	Volume connection terminal for tracking hold
41	FE	Focus error output
42	TE	Tracking error output
43	PPN	Ohms connection terminal for push-pull gain setting
44	PP	Push-pull output terminal

■ 3 **■** 4

No.	Pin name	Pin Functions
45	TS	Tracking cross signal output
46	TESI	TES comparator input terminal
47	TES	TES output
48	DEF	Deffect search
49	BHMIX	PH, BH, woble change output
50	BHACI	BH- AC input
51	ВН	RF bottom detection output
52	PH	RF peak detection output
53	woc	Volume connection terminal for DC cut
54	ISET	Ohms connection terminal for BPF center frequency setting
55	BCAI	Ohms connection terminal for peak hold detection fixed number setting (In BCA)
56	PHC	PH detection condenser connection terminal for RF-AGC
57	LPC	Condenser connection terminal for RF DC servo
58	DEFC	Volume connection terminal for deffect search
59	GND	Ground (RF system)
60	RFO	RF output terminal
61	REF	Reference output terminal
62	EQC1	Equalizer setting terminal for CD
63	VCC	Power supply terminal (RF system)
64	EQC2	Equalizer setting terminal for CD

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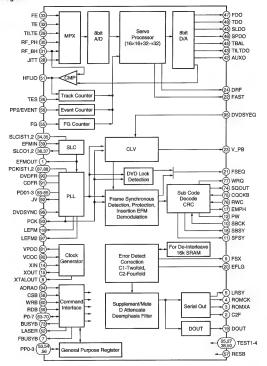
■ LC78652W (DVDM ASSY : IC201)

6

· Servo DSP IC

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Block Diagram



DV-45A

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No.	Pin Name	1/0	Pin Function				
1	EFMOUT		Output the state that was binary-stated value EFM				
2	C2F		C2 flag output				
3	ROMXA		CD-ROM data output				
4	ROMCK		Shift clock output for CD-ROM data output				
	LRSY	0	L/R clock output for CD-ROM data output				
6	PP3	1/0	General-purpose port input/output / DVD sync. signal input N ch-OD output				
	FBUSYB	0	Busy signal output of DSP process operation N ch-OD output				
8	XTALOUT	0	External system clock output				
	FSX	0	CD 1 frame sync. signal output				
	SBCK		Subcode reading out clock input				
•••	SFSY		Frame sync. signal output of subcode				
12	PW	0	Subcode P, Q, R, S, T, U, V and W output				
13	VSS	-	GND pin				
14	XIN	1	Connect a crystal resonator (16.9344MHz)				
	XOUT	0	Connect a crystal resonator				
16	DVDD1	-	3.3V power supply of the oscillation circuit				
17	EMPH	0	Monitor pin of the deemphasis				
18	SBSY	0	Sync. signal output of the subcode block				
19	DOUT	0	Audio EIAJ data output				
20	EFLG	0	Error correction state monitor of the error correction C1 and C2				
	FSEQ	0	Detection monitor of the CD/DVD frame sync. signal				
22	FAST	0	Playback speed monitor N ch-OD output				
23	V_PB	0	Monitor output of the rough servo/CLV control				
24	DRF	0	In focus monitor				
25	TEST3		Test input 3				
26	TES	1	Tracking error signal input				
27	TEST2	1	Test input 2				
28	JITT	1	Jitter quantity detecting signal input of EFM PLL				
29	TILTE	1	Tit error signal input				
30	RF_PH		RF peak hold signal input				
31	RF_BH	1	RF bottom hold signal input				
32	TE	T	Tracking error signal input				
33	FE	1	Focus error signal input				
34	SLCIST1	-	Current setting pin 1 of the constant current charge pump for SLC				
35	SLCIST2	-	Current setting pin 2 of the constant current charge pump for SLC				
36	SLCO1	0	Control output 1 for SLC				
37	SLCO2	0	Control output 2 for SLC				
38	TEST1	1	Test input 1				
39	EFMIN	1	EFM/EFM + input				
40	AVDD	-	5V power supply of A/D and D/A for servo				
41	AVSS	-	GND of A/D and D/A for servo				
42	AUXO	0	DA auxiliary output				
43	TILTDO	0	Tilt control signal output				
44	TBAL	0	Tracking balance control signal output				
45	SLDO		Sled control signal output				
46	SPDO		Spindle control signal output				
47	FDO	0	Focus control signal output				
48	TDO		Tracking control signal output				
	VREF		Reference level of D/A for servo				
50	TEST4		Test input 4				

No.	Pin Name	1/0	Pin Function		
	HFLIO	1/0	Mirror detection signal input/output		
	LASER	0	Output pin for laser ON/OFF control		
	PP0/DVD_CDB	1/0	General-purpose port input/output / Disc discrimination signal output		
	PP1/CRCERRB	1/0	Reneral-purpose port input/output / Subcode CRC result signal output		
	FG	1	Seneral-purpose port input/output / Subcode CHC result signal output		
	PP2/EVENT	1/0	General-purpose port input/output / Event counter input		
	BESB	1	Reset input		
Ψ.	CSB	i i	Chip select input		
_	RDB	i.	Internal state reading signal input		
	WRB	+	Command / data writing signal input		
	DVDD2	<u> </u>	5V power supply		
	VSS	-	GND		
	P0	H	GIND		
	P1				
65					
	P3				
	P4	1/0	Command / data input/output		
	P5				
69					
70					
	VSS		GND		
-	DVDD1	-	3.3V power supply for internal		
	BUSYB		Busy signal output of command process		
	SQOUT	0	Serial output of subcode Q		
	CQCKB		Shift clock input for subcode Q data output		
	RWC	1	Update permission input of subcode Q		
	WRQ		Read out ready monitor of subcode Q		
	AVSS	-	PLL GND for internal system clock		
	VRPFR	-	VCO oscillation range setting of PLL for system clock		
	VCOC	-1	Connect a PLL filter for system clock		
	VPDO	0	,		
	AVDD	-	PLL 5V power supply for system clock		
	PDO1		PLL filter connection pin 1 for EFM playback		
84	PDO2	1/0	PLL filter connection pin 2 for EFM playback		
	PDO3	1/0	PLL filter connection pin 3 for EFM playback		
86	AVSS	-	PLL GND for EFM playback		
	PCKIST1	-	Current setting 1 of PLL constant current charge pump for EFM playback		
	PCKIST2	-	Current setting 2 of PLL constant current charge pump for EFM playback		
	AVDD	-	PLL 5V power supply for EFM playback		
90	DVDFR	-	VCO oscillation range setting of PLL for EFM playback 1		
91	CDFR	-	VCO oscillation range setting of PLL for EFM playback 2		
92	JV	0	Jitter output of PLL clock for EFM playback		
93	PCK	0	Bit clock output for EFM playback		
94	ADRAO	1	Address input		
95	DVDSYEQ	-1	DVD synchronize pulse input		
96	DVDSYNC	1	DVD synchronous signal input		
97	LEFM2	0	Output the state that cut and out a signal which was binary-stated value EFM with PCK 2		
98	DVDD1	-	3.3V power supply for I/O		
99	VSS	-	GND		
100	LEFM	0	Output the state that cut and out a signal which was binary-stated value EFM with PCK 1		
100	LEFM	0	Output the state that cut and out a signal which was binary-stated value EFM with PCK 1		

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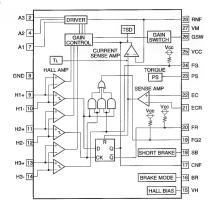
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■ BA6664FM (DVDM ASSY : IC251)

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• Three-phase Motor Driver

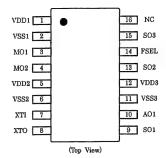
Block Diagram



No.	Pin Name	Pin Function	No.	Pin Name	Pin Function
1	N.C.	N.C.	16	BR	Brake mode switching pin
2	A3	Output pin	17	CNF	Capacitor connection pin for phase compensation
3	N.C.	N.C.	18	SB	Short brake pin
4	A2	Output pin	19	FG2	FG 3-phase mix signal output pin
5	N.C.	N.C.	20	FR	Rotation detecting pin
6	N.C.	N.C.	21	ECR	Control reference pin of output voltage
7	A1	Output pin	22	EC	Output voltage control pin
8	GND	GND pin	23	PS	Power save pin
9	H1+		24	FG	FG signal output pin
10	H1-	1	25	VCC	Power supply pin
11	H2+	1	26	GSW	Gain switching pin
12	H2-	Hall signal input pins	27	VM	Motor power pin
13	H3+	1	28	RNF	Resistor connection pin for output current detection
14	Н3-	1	FIN	FIN	GND
15	VH	Hall bias pin			

■ SM8707HV (DVDM ASSY : IC481)

- Clock Generate IC
- Pin Arrangement



No.	Pin name	Dir.	Pin Functions
1	VDD1	PWR	Power supply terminal 1 (digital business)
2	VSS1	GND	Earth terminal 1 (digital business)
3	MO1	OUT	Video output terminal 1 (the 27MHz fixed output)
4	MO2	OUT	Video output terminal 2 (the 27MHz fixed output)
5	VDD2	PWR	Power supply terminal 2 (analog business)
6	VSS2	GND	Earth terminal 2 (analog business)
7	XTI	IN	External clock input terminal or crystal resonator connection
8	хто	OUT	Crystal resonator connection terminal
9	SO1	OUT	Signal conditioning system output terminal 1 (36.8640MHz fixation)
10	AO1	OUT	Sound output terminal 1 (the 512fs output)
11	VSS3	GND	Earth terminal 3 (digital business)
12	VDD3	PWR	Power supply terminal 3 (digital business)
13	SO2	OUT	Signal conditioning system output terminal 2 (16.9344MHz fixation)
14	FSEL	IN	Sampling frequency change terminal FSEL= "L": fis=48kHz FSEL= "H": fis=44.1kHz (There is inside pull-up resister, Schmidt trigger input)
15	SO3	OUT	Signal conditioning system output terminal 3 (33.8688MHz fixation)
16	NC	-	Unused terminal

■ PD6345A (DVDM ASSY : IC601)

• FR CPU

No.		Pin Name	1/0	Pin Function
1	P20/D16	D0		
2	P21/D17	D1		
3	P22/D18	D2		
4	P23/D19	D3		
5	P24/D20	D4		
6	P25/D21	D5	1	
7	P26/D22	D6	1	
8	P27/D23	D7	1/0	Data bus input/output
9	P30/D24	D8	100	Data dus impurocuput
10	P31/D25	D9	1	
11	P32/D26	D10	1	
12	P33/D27	D11	1	
13	P34/D28	D12	1	
14	P35/D29	D13	1	
15	P36/D30	D14		
	P37/D31	D15	1	
	VSS	GND		Ground
18	P40/A00	AO		
	P41/A01	A1		
	P42/A02	A2		
	P43/A03	A3	0	
	P44/A04	A4		Address bus output
	P45/A05	A5		
	P46/A06	A6		
	P47/A07	A7		
	VCC3	V+3.3D	_	Power supply
	VCC2	V+2.5D	_	Power supply
	P50/A08	A8		
	P51/A09	A9		
	P52/A10	A10		
	P53/A11	A11		
	P54/A12	A12	0	Address bus output
	P55/A13	A13		
	P56/A14	A14		
	P57/A15	A15		
	VSS	GND		Ground
37	P60/A16	A16		un out ru
38	P61/A17	A17		
39	P62/A18	A18		
40	P63/A19	A19	0	Address bus output
	P64/A20	A20	١	Tradition and output
	P65/A21	A21	1	
	P66/A22	A22		
	P66/A22 P67/A23	WBL	0	For Worklin detection personnending to DVD DAW (main)
	DAVS			For Wobble detection corresponding to DVD R/W (main)
		GND	-	Ground
	DAVC	V+3.3D	-	Power supply
	DA0	STEP1	1	For stepping motor control
	DA1	STEP2	- 1	
49	DA2	LODRV	1 1	Loading, door and select motor drive

No.	Mark	Pin Name	1/0	Pin Function
50	AN0	NC	1	NC
51	AN1	NC		NC
52	AN2	NC	1	NC
53	AN3	XOEM	1	OEM model protection input
54	AN4	LDREAD	1	Input for LD current value indication
55	AN5	NC	1	NC
56	AN6	NC	1	NC
57	AN7	LODPOS		Loading clamp position SW input
58	AVCC	V+3.3D	-	Power supply
59	AVRH	V+3.3D	-	Power supply
60	AVSS/AVRI	GND	-	Ground
61	vss	GND	-	Ground
62	PP0/ATGX	SLDPOS	1	SW input of slider inside position
63	PP1/FRCK	GSW	0	Gain up at ACBR (at ACBR: H, others: L)
64	PP2/IN0	780ON	1	ON/OFF control signal of 780nm laser diode
65	PP3/IN1	GU	0	RF, servo signal gain up terminal (H: Gain up)
66	PP4/IN2	XMON	0	Mute of DRV (spindle motor ON: H)
67	PP5/IN3	XDRVMUT	0	FTS driver mute output
68	PP6	LT1_3V	0	Communication response to the FL controller
69	PP7	XRDY_3V	1	Communication request from the FL controller
70	VCC3	V+3.3D	-	Power supply
71	VCC2	V+2.5D	-	Power supply
72	PO0/OC0	XCURDET	1	Actuator current detection input Servo OFF for "L" 300ms
73	PO1/OC1	XCBUSY	1	Busy signal of command process Command acceptable : "L"
74	PO2/OC2	XDSPRST	0	Servo DSP reset
75	PO3/OC3	BCA	-	BCA read signal (at BCA read: H) (Not used)
76	PO4/OC4	NC	1	NC
77	PO5/OC5	PPCNT	0	Switch of TZC in WBL traversal (at PP: H)
78	P06/0C6	XDFINH	0	Defect signal control (DEFECT ON: Hi-Z; OFF: "L")
79	PO7/OC7	DPD/TE	0	H=1 beam, L=3 beams
80	VSS	GND	-	Ground
81	PN0/AIN0	DVD/XCD	0	RF EQ switching signal at DVD/CD "H": DVD, "L": CD
82	PN1/BIN0	AGOFF	0	"H": Tum off AGC of RFIC
83	PN2/AIN1	650X780	0	780nm/650nm switching signal
84	PN3/BIN1	LD ON	0	ON/OFF control signal of laser diode
85	PN4/AIN2	WBLSEL	0	NC
86	PN5/BIN2	RFSEL	0	RF amplifier gain change terminal (H: Gain up)
87	PN6/AIN3	XCD2X	0	For VCD double speed playback
88	PN7/BIN3	OEICG	0	"H": Gain of OEIC up to 6dB
89	PM0/ZIN0	EN33M	0	NC .
90	PM1/ZIN1	EN24M	0	NC .
91	PM2/ZIN2	V SEL	0	(Composite, S) / (YCbCr) or (RGB) switch
92	PM3/ZIN3	V SEL2	0	(Composite) of scart terminal / (S) switch
93	PL0/SDA1	SDAI	12C Serial	12C control lines
94	PL1/SDA0	NC	-	NC .
95	PL2/SCL1	SCLI	12C Serial	12C control lines
96	PL3/SCL0	NC	-	NC .
97	PL4	CTS	1	RS-232C clear to send input
98	PL5	DTR	0	RS-232C clear to send output
99	PL6/UC0	NC	0	NC
100	vss	GND	-	Ground

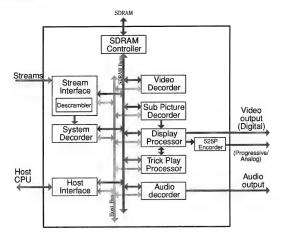
No.	Mark	Pin Name	I/O	Pin Function	
101	PK0/TIN0	XVQERST	0	VQE3 reset signal	
102	PK1/TIN1	XCSPRO1	-	Serial communication enable of the progressive converter IC	
103	PK2/TIN2	XCSVQE5	_	Serial communication enable of VQE5 IC	
104	PK3/TIN3	EN16M	0	N.C.	
105	PK4/TOT0	44X48	0	DAC and DASP supply clock fs 44/48 selection	
106	PK5/TOT1	1394XRDY	- 1	N.C.	
107	PK6/TOT2	AOSEL1	0	AV-1/audio DSP switch (front L/R data)	
108	РК7/ТОТЗ	P/XI	0	Progressive/Inter race change signal	
109	VCC3	V+3.3D		Power supply	
110	VCC2	V+2.5D	-	Power supply	
111	PJ0/INT0	XINTO	- 1		
112	PJ1/INT1	XINT1			
113	PJ2/INT2	XIRQ10	<u> </u>	MY chip interrupt #0	
114	PJ3/INT3	XIRQ11		MY chip interrupt #1	
	PJ4/INT4	XABUSY	- i	Busy signal of DSP process operation "L"	
	PJ5/INT5	THLD		Playback speed monitoring signal	
	PJ6/INT6	SBSY	1	Sync. signal of subcode block (period SO+SI "H")	
	PJ7/INT7	N.C.	-	N.C.	
	PIO/SIO	ISSI	<u> </u>	Serial bus data input	
	PI1/SO0	SSO 3V	0	Serial bus data output	
	PI2/SCK0	SSCK_3V	1	Serial bus clock input	
	PI3/SI1	RXD_3V	-i	RS-232C RXD	
_	PI4/SO1	TXD 3V	0	RS-232C TXD	
	PI5/SCK1	NC NC	0	NC	
	PH0/SI2	1394LT		NC NC	
	PH1/SO2	DSPICM	-	Audio system DSP serial communication Ready signal	
	PH2/SCK2	NC NC		NC	
	MD0	GND	<u></u>	NC .	
	MD1	GND		Ground	
	MD2	GND		Ground	
	VSS	GND		Ground	
	VCC2	V+2.5D		Power supply	
	VSS	GND		Ground	
134		EXTAL	- 0	Ground	
135		XTAL	1		
-		V+3.3D			
	VCC3 PC0/DREQ2	RESET1		Power supply	
	PC0/DHEQ2 PC1/DACK2	XCSADSP0	0	Audio system DSP reset Chip select port for audio system DSP	
	PC2/DEOP2	XCSDF2	0	DAC chip select (for surround system L/R)	
	PB0/DREQ0	XDREQ0	1	DMA response output to BY Chip	
	PB1/DACK0	DACK0	0	DMA request input from BY Chip	
	PB2/DEOP0	ENCD	0	N.C.	
	PB3/DREQ1	XDREQ1	- 1	DMA response output to AV-1 Chip	
_	PB4/DACK1	XDACK1	0	DMA request input from AV-1 Chip	
	PB5/DEOP1	EN_FLOW	0	N.C.	
	PB6/IOWRX	XCOMP	0	RGB/color difference change of video driver	
147	PB7/IORDX	XCSDF3	0	N.C.	
		GND	-	Ground	
148	VSS PA0/CSOX	XCS20	0	Chip select output to Flash ROM	

No.	Mark	Pin Name	VO	Pin Function	
151	PA2/CS2X	XCS3	0	Chip select of PD4995A (MY Chip)	
152	PA3/CS3X	XCS4	0	Chip select of servo DSP	
153	PA4/CS4X	XCS23	0	Chip select output to SRAM (1M)	
154	PA5/CS5X	N.C.	0	N.C.	
155	PA6/CS6X	N.C.	0	N.C.	
156	PA7/CS7X	N.C.	0	N.C.	
157	VCC3	V+3.3D	-	Power supply	
158	VCC2	V+2.5D	-	Power supply	
159	NMIX	-	-	V+3.3D fixed	
160	HSTX	-	-	V+2.5D fixed	
161	INITX	XINIT	1		
162	P80/RDY	RDY	1		
163	P81/BGRNTX	XAMUTE	T	Final stage mute of 2 ch audio output	
164	P82/BRQ	XMMUTE	0	Audio multi channel mute	
165	P83/RDX	XRD	0		
166	P84/WR0X	XWR0	0		
167	P85/WR1X	XWR1	0		
168	VSS	GND	-	Ground	
169	P90/SYSCLK	SYSCLK	0	N.C.	
170	P91	DFRST	-	DAC reset (for front L/R)	
171	P92/MCLK	DFRST1	-	DAC reset (for center, surround and LFE)	
172	P93	XCSDF0	0	DAC chip select (←XLAT3)	
173	P94/LBAX	XCSDF1	0	DAC chip select for center, surround and LFE	
174	P95/BAAX	XAQRST	0	AQE reset	
175	P96	XCSAQE	0	AQE chip select	
176	P97/WEX	TM ENT	- 1	Test mode entry	

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- M65776AFP (DVDM ASSY : IC751)
 - MPEG2 Decorder IC
- Block Diagram

1



DV-45A

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No.	Pin name	Dir.	Pin Functions		
201-208	BD [7:0]	IN	Bit stream data entry pin		
2	BCLK	IN	Strobe signal of BD pin (clock)		
3	BDEN	IN	This order effective / invalidity of data done a sample of by BD pin. It is done a sample with a start edge of BCLK.		
4	BDREQ	OUT	Data demand signal		
5	BSECH	IN	This order it whether data of BD pin are with top byte of a sector.		
84-87 90-95 97-102	MD [15:0]	1/0	Data transfer line with SDRAM		
53-55 58-63 65, 67, 69	MA [11:0]	OUT	Address line of SDRAM		
66, 68	MBA [1:0]	OUT	SDRAM bank choice line		
70	DCS				
73	DCS2				
74	DCS3	OUT	Chip select of SDRAM		
75	DCS4				
76	DCS5				
77	RAS	OUT	RAS (Row Address Strobe) control line of SDRAM		
78	CAS	OUT	CAS (Column Address Strobe) control line of SDRAM		
82	DQMU	OUT	DQM control line of SDRAM		
83	DQML	OUT	DQM control line of SDRAM		
80	DWE	OUT	WE control line of SDRAM		
79	MCLK	OUT	Movement clock of SDRAM		
183	PXCLK	OUT	27MHz pixel clock		
182	PXCLKP	OUT	54MHz pixel clock		
157, 158, 184-186 188-192	PD [7:0]	оит	Digital pixel data. Y/Cb/Cr is done multiple of by 8 bit bus, and it is output.		
178	CSYNC	IN	Composite SYNC signal input terminal		
179	OSDKEY	OUT	OSD key flag output		
177	PWD	OUT	The phase comparator output for external synchronization movement		
181	HSYNC	OUT	Horizontal synchronizing signal output pin		
180	VSYNC	OUT	Vertical synchronizing signal output pin		
164	A00	OUT	Serial PCM data for DAC It output Lt/Rt data.		
166	AO1	OUT	Serial PCM data for DAC It output C/Sw data.		
167	AO2	OUT	Serial PCM data for DAC It output Ls/Rs data.		
168	AOD	OUT	Serial PCM data for DAC It is for the down mixture output.		
169	AAD	OUT	Anciallary data output		
176	DOCLK	OUT	PCM bit clock		
159	LRCLK	OUT	Clock for channel distinction of pulse code modulation audio system data (L/R)		
173	DACCLK	OUT	Exaggerated sample movement clock of DAC		
161	CDBCK	IN	The pulse code modulation bit clock which is input by CDDSP		
160	CDLRCK	IN	The L/R clock which is input by CDDSP		

No.	Pin name	Dir.	Pin Functions			
163	CDDIN	IN	PCM audio system data which are input by CDDSP			
162	CDDATA	IN	Digital audio interface input			
170	DOUTO	OUT	Digital audio interface output			
171	DOUT1	OUT	Digital audio interface output			
6-11 14-19 21-24	HD [15:0]	1/0	Data I/O pin			
25, 26 29-34 36-39	HA [11:0]	IN	Address input pin			
45	BHE	IN	Byte High Enable signal input pin			
41	RE	IN	Read Enable signal input pin			
44	WE	IN	Write Enable signal input pin			
40	cs	IN	Chip Select signal input pin			
46	RDY	OUT	The acknowledge signal which shows that readout of data or a note was completed			
47	INT1					
48	INT2	OUT	It is an interrupt request signal for outside CPU from M65776AFP			
49	INT3					
51	DREQ	OUT	DMA request signal for OSD BitMap transfer			
52	DACK	IN	DMA acknowledge signal for OSD BitMap transfer			
194, 195	HMODE [1:0]	IN	Host interface mode of operation setting pin			
117	IREF	IN	Reference electric current input pin			
115	AVRI	IN	Reference voltage input pin			
120	BIAS1					
118	BIAS2	IN	Bias voltage impression pin of current source			
119	PAY	OUT	Analog electric current output pin (for Y)			
116	PAB	OUT	Analog electric current output pin (for Pb)			
122	PAR	OUT	Analog electric current output pin (for Pr)			
114	DAOUTB	OUT	Be connected to an analog ground.			
113, 121, 123	AVDD33	-	3.3V analog power supply			
124	AGND33	-	Analog ground			
106	CLKIN	IN	System clock input terminal It input 27MHz clock.			
105	CLKO	OUT	27MHz clock output			
172	ACLKI	IN	Audio system clock input terminal			
193	RESET	iN	Hardware reset terminal			
196, 197, 200	TEST [2:0]	IN	Fix it in "L" potential.			
12, 27, 42, 56, 71, 88, 103, 134, 155, 174, 198	VDD18	-	1.8V power supply terminal			
13, 28, 43, 57, 72, 89, 104, 135, 156, 175, 199	VDD33	-	3.3V power supply terminal			

3

С

D

Ε

No.	Pin name	Dir.	Pin Functions
1, 20, 35, 50, 64, 81, 96, 112, 125, 145, 165, 187	GND	-	Ground terminal
107	AVDD18	-	1.8V power supply terminal for inside PLL
108	AGND18	-	Ground terminal for inside PLL
109-111 126-133 136-144 146-154	NCO	NC	

5

С

D

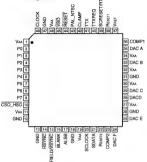
■ AD7172KST (DVDM ASSY : IC801)

• Digital PAL/NTSC Video Encoder with Six DACs (10-bits), Color Control and Enhanced Power Management

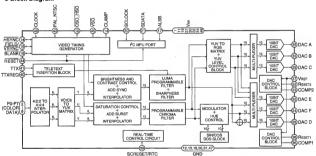
2

Pin Arrangement

1



Block Diagram



No.	Name	1/0	Pin Function
1	VAA	Р	Power Supply (+3V to +5V)
2	P0		
3	P1	١.	8-bit 4 : 2 : 2 Multiplexed YCrCb Pixel Port (P7-P0) P0 represents the LSB
4	P2	'	6-bit 4 . 2 . 2 Multiplexed TO/CO Fixer For (F7-F0) Fo represents the LSB
5	P3		

No.	Name	I/O	Pin Function
6	P4	+	
7	P5	١.	
8	P6	- '	8-bit 4 : 2 : 2 Multiplexed YCrCb Pixel Port (P7-P0) P0 represents the LSB
9	P7	1	
10	CSO_HSO	0	Dual function CSO or HSO TTL Output Sync Signal
11	VAA	P	Power Supply (+3V to +5V)
12	GND	G	Ground Pin
13	GND	G	Ground Pin
14	HSYNC	1/0	HSYNC (Models 1 and 2) Control Signal. This pin may be configured to output (Master Mode) or as an input and accept (Slave Mode) Sync signals.
15	FIELD/VSYNC	1/0	Dual Function FIELD (Mode1) and VSYNC (Mode2) Control Signal. This pin may be configured to output (Master Mode) or as an input (Stave Mode) and accept these control signals.
16	BLANK	1/0	Video Blanking Contrl Signal. The pixel inputs are ignored when this is logic level "0". This signal is optional.
17	ALSB	- 1	TTL Address Input. This signal sets up the LSB of the MPU address.
18	GND	G	Ground Pin
19	VAA	P	Power Supply (+3V to +5V)
20	SCLOCK	1	MPU Port Serial Interface Clock Input
21	SDATA	1/0	MPU Port Serial Data Input/Output
	RSET2	1	A 600 ohm resistor connected from this pin to GND is used to control full-scale amplitudes of the Video Signals from DACs D, E and F (the "small" DACs).
23	COMP2	0	Compensation Pin for DACs d, E and F. Connect a 0.1µF Capacitor from COMP to VAA.
24	DAC F	0	RED/S-Video C/V Analog Output. This DAC is capable of providing 8.66 mA output.
25	DAC E	0	BLUE/S-Video Y/U Analog Output. This DAC is capable of providing 8.66 mA output.
26	GND	G	Ground Pin
27	VAA	P	Power Supply (+3V to +5V)
28	DAC D	0	GREEN/Composite/Y Analog Output. This DAC is capable of providing 8.66 mA output.
29	DAC C	0	RED/S-Video C/V Analog Output. This DAC is capable of providing 34.66 mA output.
30	VAA	P	Power Supply (+3V to +5V)
31	GND	G	Ground Pin
32	VAA	P	Power Supply (+3V to +5V)
33	DAC B	0	BLUE/S-Video Y/U Analog Output. This DAC is capable of providing 34.66 mA output.
34	VAA	P	Power Supply (+3V to +5V)
35	DAC A	0	GREEN/Composite/Y Analog Output. This DAC is capable of providing 34.66 mA output.
36	COMP1	0	Compensation Pin for DACs A, B and C. Connect a 0.1µF Capacitor from COMP to VAA. For Optimum Dynamic Performance in Low Power Mode, the value of the COMP1 capacitor can be lowered to as low as 2.2mF.
37	VREF	1/0	Voltage Reference Input for DACs or Voltage Reference Output (1.235V)
38	RSET1	1	A 150 ohm resistor connected from this pin to GND is used to control full-scale amplitudes of the Video Signals from DACs A, B and C (the "large" DACs).
39	SCRESET/RTC	1	This pin can be configured as an input by setting MR42 and MR41 of Mode Resistor 4. It can be configured as a subcarrier reset pin, in which case a high to low transition on this pin will reset the subcarrier phase to Field 0. Alternatively it may be configured as a Real-Time Control (RTCF) input.
40	TTXREQ	0	Teletext Data Request input signal used to control teletext data transfer.
41	TTX	0	Teletext Data Input Pin.
42	CLAMP	0	TTL Output Signal to external circuitry to enable clamping of all video signals.
43	PAL_NTSC	1	Input signal to select PAL or NTSC mode of operation, pin set to Logic *1* selects PAL.
44	RESET	ı	The input resets the on-chip timing generator and sets the ADV7172KST into default mode. This is NTSC operation, Timing Slave Mode 0, DACs A, B and C powered OFF, DACs D, E and F powered ON, Composite and S-Video out.
45	VSO	0	VSO TTL Output Sync Signal
46	VAA	Р	Power Supply (+3V to +5V)
47	GND	G	Ground Pin
48	CLOCK	1	TTL Clock Input. Hequires a stable 27 MHz reference clock for standard operation. Alternatively, a 24.52 MHz (NTSC) or 29.5 MHz (PAL) can be used for square pixel operation.

■ PCM1738EG-3 (JACB ASSY : IC301)

• D/A Converter IC

Pin Arrangement

	PCM1738						
1	RST	V _{CC} 3	28				
2	ZEROL	AGND2	27				
3	ZEROR	lourL-	26				
4	LRCK	lourL+	25				
5	DATA	V _{cc2}	24				
6	BCK	Voc1	23				
7	scki	V _{COM} 3	22				
8	DGND	IREF	21				
9	Voo	V _{COM} 2	20				
10	sско	V _{COM} 1	19				
11	MDO	AGND1	18				
12	MDI	lourR+	17				
13	MC	lourH-	16				
14	₹ ē	MUTE	15				

Pin Function

PIN	NAME	TYPE	DESCRIPTIONS	_
1	RST	- IN	Reset	(10
2	ZEROL	OUT	Zero Flag for L-channel	
3	ZEROR	OUT	Zero Fing for R-channel	
4	LRCK	IN	Left and Right Clock (§) Input for Normal operation. WDCK clock input in External DF mode. Connected to GND in DSD mode.	(10
5	DATA	IN	Serial Audio Data Input for Normal operation. L-channel audio data input for External DF and DSD modes.	(1)
6	BCK	IN	Bit Clock, Input. Connected GND for DSD mode.	(1)
7	SCKI	IN	System Clock Input. BCK (64 f ₈) clock input for DSD mode	(1)
8	DGND		Digital Ground	_
9	V _{DD}	- 1	Digital Supply, +3.3 V	_
10	SCKO	OUT	System Clock Output	
11	MDO	OUT	Serial data output for function control register	(1)
12	MDI	IN	Serial data input for function control register	
13	MC	IN	Shift Clock for function control register	H
14	CS	IN	Mode control chip select and latch signal.	
15	MUTE	in -	Analog output mute control for normal operation R-channel audio data input for external DF mode and DSD mode.	m
16	loutR-	OUT	R-channel Analog Current Output -	
17	lourR+	OUT	R-channel Analog Current Output +	
18	AGND1		Analog Ground.	
19	V _{cos} 1		Internal bias de-coupling pin	
20	V _{cow2}		Common voltage for I/V	
21	Iver	-	Output current reference bias pin. Connect 16KQ resistor to GND	
22	V _{COM} 3	-	Internal bias de-coupling pin	
23	V _{cc} 1		Analog Supply, +5.0 V	
24	V _{cc} 2	-	Analog Supply, +5.0 V	_
25	lourL+	OUT	L-channel Analog Current Output +	_
26	lourL-	OUT	L-channel Analog Current Output -	
27	AGND2	-	Analog Ground	_
28	Vcc3	-	Analog Power Supply, +5.0V	

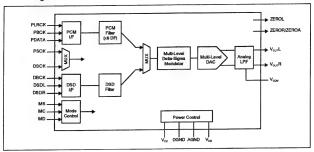
NOTE

Schmitt trigger input, 5 V tolerant,

) Tristate output

■ DSD1702EG (JACB ASSY : IC401, IC501)

- D/A Converter IC
- Block Diagram



Pin Arrangement

1	DSDL	DBCK	20
2	DSDR	DSCK	19
3	PBCK	PSCK	18
4	PDATA	MS	17
5	PLRCK	MC	16
6	DGND	MD	15
7	Vop	ZEROR/ZEROA	14
8	Vcc	ZEROL/NA	13
9	VourL	Vcoм	12
10	VoutR	AGND	11

PIN					
1	DSDL	IN	Audio data digital input (DSD L-channel)		
2	DSDR	IN	Audio data digital input (DSD R-channel)		
3	PBCK	IN	Audio data bit clock input. (PCM)	TU	
4	PDATA	IN	Audio data digital input. (PCM)	(1)	
5	PLRCK	IN	Audio data latch enable input. (PCM)	(U	
6	DGND		Digital ground.		
7	Vpp		Digital power supply, + 3.3 V.		
8	Voc		Analog power supply, + 5 V.		
9	VourL	OUT	Analog output for L-channel.		
10	VoutR	OUT	Analog output for R-channel.		
11	AGND		Analog ground.		
12	Vcow		Common voltage decoupling.		
13	ZEROR/ZEROA	OUT	Zero flag output for R-channel / Zero flag output for L/R-channel.		
14	ZEROL/NA	OUT	Zero flag output for L-channel / No assign.		
15	MD	1N	Mode control data Input.	(2)	
16	MC	łN	Mode control clock input.		
17	MS	IN	Chip Select for Mode control.		
18	PSCK	IN	System clock input. (PCM) (0		
19	DSCK	IN	System clock input. (DSD)		
20	DBCK	IN	Audio data bit clock input. (DSD)	tu	

- Schmidt trigger input, 5 V tolerant.
 Schmidt trigger input with internal pull-down, 5 V tolerant.

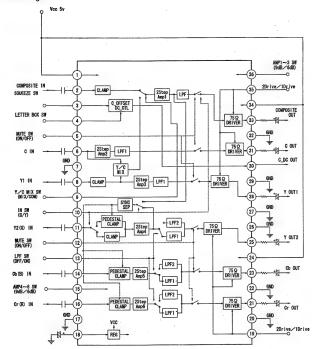
■ LA73054 (JACB ASSY : IC701)

2

DVD Video Amplifier

Block Diagram

1



D

No.	Pin Fu	nctions	0- 0.7V (LOW)	2.6- 5V (HIGH)
36	AMP-GAIN chang	e for composite/S	6 dB	9 dB
15	AMP-GAIN chan	ge for component	6 dB	9 dB
35	Drive electric current of	hange for composite/S	2 system drive	1 system drive
19	Drive electric current	change for component	2 system drive	1 system drive
	Mute control for	In 10 pin LOW	It is not do mute	33, 31, 28 pin mute
5	composite/S	In 10 pin HIGH	It is not do mute	31, 28 pin mute
12	Mute control	for component	It is not do mute	25, 23, 21 pin mute
9	The control	of Y/C- MIX	In composite	In Y/C MIX
10	11 pin input	form change	In the component input	In the baseband input
13	LPF characteristic cl	nange for component	Inter race correspondence	Progressive correspondence

² pin falls to GND in Y/C-MIX.

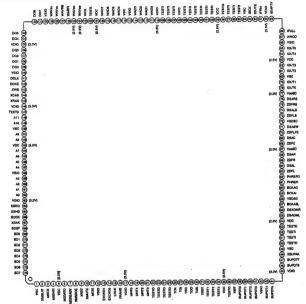
¹¹ pin is clamp, and the Y signal input, 14, 16 pin input a CB, CR signal into NTSC (in the component input) with pedestal clamp. 8 pin is clamp, and the Y signal input, 11, 14, 16 pin input a R, G, B signal into PAL (in the baseband input) with pedestal clamp. It prohibit mute of 5 pin when it of V/C-MIX in PAL (in the baseband input).

CXD2753R (SACDB ASSY : IC901)

2

- SACD Decorder
- Pin Arrangement

1



Pin Function

No.	Pin Name	1/0	Pin Function	
1	VSC	-	Ground terminal for core	
2	XMSLAT		Latched input terminal for microcomputer serial communication	
3	MSCK	1	Shift clock input terminal for microcomputer serial communication	
4	MSDAI	1	Data entry terminal for microcomputer serial communication	
5	VDC	-	Power supply terminal for core	
6	MSDATO		Data output terminal for microcomputer serial communication	
7	MSREADY	0	Output preparation completion flag for microcomputer serial communication	
8	XMSDOE	1	Output enable terminal for microcomputer serial communication	
9	XRST	T	Reset terminal resets the whole IC with "L".	
10	SMUTE	Ipd	Software mute removes audio out with "L" with "H" a soft mute terminal.	
11	MCKI	1	Master clock input terminal	
12	VSIO	-	Ground terminal for I/O	
13	EXCKO1		Outside output clock terminal 1	
14	EXCKO2		Outside output clock terminal 2	
15	LRCK	1 0	1Fs (44.1kHz) clock output terminal	
16	FRAME	1	Frame signal output terminal	
17	VDIO	-	Power supply terminal for I/O	
18	MNT0			
19	MNT1	1	Monitor output terminal	
20	MNT2	0		Worker output terminal
21	MNT3			
22				
23	TESTO		Output terminal for test	
24	IESIO		- 1	Output terminal for test
25				
26	TCK	1	It is fixation in "L" a clock input terminal for test.	
	TDI	lpu	Input terminal for test	
	VSC	-	Ground terminal for core	
	TDO	0	Output terminal for test	
	TMS	lpu	Input terminal for test	
	TRST	,pu	Reset terminal for test	
	TEST1			
	TEST2	1	It is fixation in "L" a clock input terminal for test.	
	TEST3			
	VDC		Power supply terminal for core	
	TESTO		Output terminal for test	
	XBIT		DST connection monitor terminal	
	SUPDT0	0	Supplementary data output terminal (LSB)	
	SUPDT1			
	SUPDT2		Supplementary data output terminal	
	SUPDT3	_		
	VSIO	Ŀ	Ground terminal for I/O	
	SUPDT4	0	Supplementary data output terminal	
	SUPDT5			
	VDIO	Ŀ	Power supply terminal for I/O	
	SUPDT6		Supplementary data output terminal	
	SUPDT7	0	Supplementary data output terminal (MSB)	
	XSUPAK	_	Supplementary data output terminal	
	VSC	-	Ground terminal for core	
50	TESTO	0	Output terminal for test	

No.	Pin Name	I/O	Pin Function
51 52	TESTI	1	It is fixation in "L" a test input terminal.
53	TESTO	0	Output terminal for test
54	VDC	1	Power supply terminal for core
55	DSADML	١.	DSD data output terminal for Lch Down Mix
56	DSADMR	0	DSD data output terminal for Rch Down Mix
57	BCKASL	T	Input and output choice terminal of a 1 bit clock for DSD data output.L= input (slave), H = output (master),
58	VSDSD	-	Ground terminal for DSD data output
59	BCKAI	ī	Bit clock input terminal for DSD data output
60	BCKAO		Bit clock output terminal for DSD data output
61	PHREFI		Phase reference signal input terminal for DSD output phase modulation
62	PHREFO	-	Phase reference signal output terminal for DSD output phase modulation
63	ZDFL	1	Zero Lch data search flag
64	DSAL	0	DSD data output terminal for Lch loud speaker
	ZDFR	-	Zero Rch data search flag
66	DSAR		DSD data output terminal for Rch loud speaker
67	VDDSD	-	Power supply Mizuko for DSD data output
	ZDFC	-	Zero Cch data search flag
_	DSAC		DSD data output terminal for Cch loud speaker
	ZDFLFE	0	Zero LFEch data search flag
	DSASW	1	DSD data output terminal for SWch loud speaker
	VSDSD	-	Ground terminal for DSD data output
	ZDFLS	-	Zero LSch data search flag
	DSALS	-	DSD data output terminal child for LSch loud speaker
•	ZDFRS	0	Zero RSch data search flag
	DSARS		DSD data output terminal for RSch loud speaker
	VDDSD	-	Power supply Mizuko for DSD data output
	IOUTO	Ŀ	Data output terminal 0 for IEEE1394 link tip I/F
	IOUT1	0	Data output terminal of for IEEE1394 link tip I/F
_	VSC	-	Ground terminal 1 for IEEE 1394 link tip I/P
	IOUT2	Ŀ.	Data output terminal 2 for IEEE1394 link tip I/F
		0	
	IOUT3	_	Data output terminal 3 for IEEE1394 link tip I/F
		Ŀ	Power supply terminal for co
	IOUT4	0	Data output terminal 4 for IEEE1394 link tip I/F
		-	Data output terminal 5 for IEEE1394 link tip I/F
	VSIO	_	Ground terminal for I/O
	IANCO	0	Transmission information data output terminal for IEEE1394 link tip I/F
	IFULL	1.1	Data transmission hold demand signal input terminal for IEEE1394 link tip I/F
	IEMPTY	_	High speed transmission demand signal input terminal for IEEE1394 link tip I/F
	VDIO	Ŀ	Power supply terminal for I/O
_	IFRM		Frame reference signal output Mizuko for IEEE1394 link tip I/F
	IOUTE	0	Enable signal output terminal for IEEE1394 link tip I/F
	IBCK		Data transmission clock output terminal for IEEE1394 link tip I/F
	VSC	-	Ground terminal for core
95		1	It is fixation in "H" a test input terminal.
	TESTI	Ľ.	It is fixation in "L" a test input terminal.
97			It is fixation in "H" a test input terminal.
	TESTO		Output terminal for test
	VDC		Power supply terminal for co
nn	TESTI		It is fixation in "L" a test input terminal.

No.	Pin Name	1/0	Pin Function					
101		_						
102								
103	TESTI	1	It is fixation in "L" a test input terminal.					
104		1	·					
105								
106	VSIO	-	Ground terminal for I/O					
107		_						
108	TESTI	1	It is fixation in "L" a test input terminal.					
109								
110	VDIO	-	Power supply terminal for I/O					
111	WAD0		Outside A/D data entry terminal for PSP Physical Disc Mark search (LSB)					
112	WAD1							
113	WAD2	١'	Outside A/D data entry terminal for PSP Physical Disc Mark search					
114	WAD3							
115	VSIO	-	Ground terminal for I/O					
116	VSC	-	Ground terminal for core					
	WAD4							
118	WAD5		Outside A/D data entry terminal for PSP Physical Disc Mark search					
	WAD6] '						
120	WAD7		Outside A/D data entry terminal for PSP Physical Disc Mark search (MSB)					
	VDC	-	Power supply terminal for core					
	TESTI	1	It is fixation in "L" a test input terminal.					
123	WCK	Ŀ	Movement clock for PSP Physical Disc Mark search					
124 125	WAVDD	-	A/D power supply terminal for PSP Physical Disc Mark search					
126	WARFI	Ai	Analog RF signal input terminal for PSP Physical Disc Mark search					
127	WAVRB	Α.	A/D bottom reference terminal for PSP Physical Disc Mark search					
128	WAVSS	-	A/D ground terminal for PSP Physical Disc Mark search					
130	VSIO	-	Ground terminal for I/O					
131	DQ7		SDRAM data input-output terminal (MSB)					
132	DQ6	1/0						
133	DQ5	1 "	SDRAM data input-output terminal					
134	DQ4	1						
	VDIO	-	Power supply terminal for I/O					
	DQ3							
	DQ2	1/0	SDRAM data input-output terminal					
	DQ1	1 "						
	DQ0		SDRAM data input-output terminal (LSB)					
	VSIO	-	Ground terminal for I/O					
	DCLK	1	Clock output terminal for SDRAM					
	DCKE		Clock enable output terminal for SDRAM					
	XWE	0	Wright enable output terminal for SDRAM					
	XCAS	1	Column address strobe output terminal for SDRAM					
	XRAS	_	Row address strobe output terminal for SDRAM					
	VDIO	-	Power supply terminal for I/O					
	TESTO	1	Output terminal for test					
		0	Address output terminal for SDRAM (MSB)					
		1_	Address output terminal for SDRAM					
150	VSC	-	Ground terminal for core					

С

D

N	lo.	Pin Name	1/0	Pin Function	
1	151 A9	0	Address output terminal for SDRAM		
1	52	A8	1	Address dupat termina for our vivi	
1	53	VDC		Power supply terminal for core	
1	54	A7			
1	55	A6	0	Address output terminal for SDRAM	
1	56	A5	1 0	Address output terminal for SDNAW	
1	57	A4	1		
1	58	VSIO		Ground terminal for I/O	
1	59	A3			
1	60	A2	٦	Address output terminal for SDRAM	
1	61	A1	١ ٠		
1	62	A0	1	Address output terminal for SDRAM (LSB)	
1	63	VDIO	-	Power supply terminal for I/O	
1	64	XSRQ	0	Data request output terminal to input into a front end processor	
1	65	XSHD		Input terminal of a header flag output by a front end processor	
1	66	SDCK	1	Input terminal of a data carrier clock output by a front end processor	

Input terminal of data partial response flag output by a front end processor

The stream data input terminal which is output by a front end processor

The stream data input terminal which is output by a front end processor (LSB)

The stream data input terminal which is output by a front end processor (MSB)

Input terminal of error flag output by a front end processor

167 XSAK

168 SDEF

169 SD0

170 SD1

■ PE5314B (FLKY ASSY : IC101)

• FL Controller

Pin Function

No.	Signal name	Dir.	Pin Functions	
1	VDD1	-	Positive Power Supply (3.3 V)	
2	Vss1	-	Ground Potential	
3	X1	IN	Crystal Connection for Main System Clock Oscillation	
4	X2	-	Crystal Collinection for Main System Clock Oscillation	
5	IC	-	Internally Connected (Directly connect to VSS1)	
6	RESET	IN	Reset Input	
7	SCK1	IN	Serial Clock Input of Serial Interface	
8	SI1	IN	Serial Data Input of Serial Interface	
9	SO1	OUT	Serial Data Output of Serial Interface	
10	XRDY	OUT	Hand-shake (Ready) Output of Serial Interface	
11	POWER ON	OUT	Power Control Output	
12	RESET OUT	OUT	System Reset Output	
13	RESERVE OUT	OUT	Reserved (NC on this model)	
14	LED8	OUT	LED Port 8 (NC on this model)	
15	HALT	IN	Halt Port "NC": Use Halt Mode	
16	ACK	IN	Hand-shake (Acknowledge) Input of Serial Interface (Interrupt)	
17	SEL IR	IN	Remote Control Input (Timer input of 8-bit remote control timer)	
18	Avss	-	Ground Potential for A/D Converter	
19	MS1	IN	Destination (of player) Select (Analog Input for A/D Converter)	
20	NC	-	NC	
21	KEY1	IN	Key Input 1 (Analog input for A/D converter)	
22	KEY0	IN	Key Input 0 (Analog input for A/D converter)	
23	VSS0	_	Ground Potential to Ports	
24	AVDD	-	Analog Power/Reference Voltage Input to A/D Converter (3.3 V)	
25	VDD0	-	Positive Power Supply to Ports (3.3 V)	
26	MS0_2			
27	MS0_1	1N	Model (of player) Select (Set with a combination of this 3 ports)	
28	MS0_0			
29	LED7	OUT	LED Port 7	
30	LED(STAND BY)	OUT	Stand By LED Port	
31	PWSW	IN	Primary Switch State Input "H": ON "L": OFF	
32	TES	- IN	"H" : No System Reset mode "L" : General mode	
33	OEM	IN	"H" : OEM Model "L" : Pioneer Model	
34	MIC IN	IN	Detection of Microphone "H": Microphone connected	
35	CHECKER	IN	"H" : Checker Mode "L" : General mode	
36	ON POWER	IN	"H" : Primary Power Switch Model "L" : Secondary Power Switch Model	
37	FL SET2	IN	FL-Controller Mode Select FL-SET1 / 2 = "H" / "H" : Other model FL-SET1 / 2 = "H" / "L" : Other model	
38	FL SET1		FL SET1 / 2 = "L" / "H" : Other model FL SET1 / 2 = "L" / "L" : DV-555, 656A, 757Ai (This model)	
39	TEST2	OUT	Test Port	
40	LED6	OUT	LED Port 6	

D

No.	Signal name	Dir.	Pin Function
	LED5		LED Port 5
42	LED4		LED Port 4
43	LED3		LED Port 3 (NC on this model)
44	LED2	OUT	LED Port 2 (NC on this model)
45	LED1	1	LED Port 1 (NC on this model)
46	LED0	1	LED Port 0 (NC on this model)
47	TEST1	OUT	Test Port
48	NC	-	NC .
49	1394RST	OUT	1394 Host Controller Reset Output
50	NC	-	NC NC
51	P16	OUT	FIP Segment 16 Output
52	P15	OUT	FIP Segment 15 Output
53	NC	-	NC
54	P14		FIP Segment 14 Output
55	P13	1	FIP Segment 13 Output
56	P12	OUT	FIP Segment 12 Output
57	P11	1	FIP Segment 11 Output
58	P10	1	FIP Segment 10 Output
59	VDD2	-	Positive Power Supply to FIP Controller/Driver (3.3 V)
60	VLOAD	-	Pull-down Resistor Connection of FIP Controller/Driver (-28V)
61	P9		FIP Segment 9 Output
62	P8	1	FIP Segment 8 Output
63	P7	1	FIP Segment 7 Output
64	P6	1	FIP Segment 6 Output
65	P5	OUT	FIP Segment 5 Output
66	P4	1	FIP Segment 4 Output
67	P3		FIP Segment 3 Output
68	P2	1	FIP Segment 2 Output
69	P1	1	FIP Segment 1 Output
70	G11		FIP Grid 11 Output
71	G10		FIP Grid 10 Output
72	G9	1	FIP Grid 9 Output
73	G8	1	FIP Grid 8 Output
74	G7		FIP Grid 7 Output
75	G6	OUT	FIP Grid 6 Output
76	G5	1	FIP Grid 5 Output
77	G4]	FIP Grid 4 Output
78	G3]	FIP Grid 3 Output
79	G2		FIP Grid 2 Output
80	G1	-	FIP Grid 1 Output

DVD Data Processor

5

Pin Function

No.	Pin name	Dir.	Pin Functions
3, 40, 50, 54, 84, 103, 107, 145, 154, 158, 207		-	It is a power supply of digital circuit. Be connected to +3.3V.
15, 18, 27, 53, 64, 74, 78, 92, 104, 130, 157, 164, 183, 191, 208		-	It is a power supply of digital dircuit. Be connected to +2.5V.
1, 2, 16, 17, 26, 41, 51, 52, 63, 73, 79, 85, 91, 105, 106, 131, 144, 150, 155, 156, 178, 182, 190		-	It is a ground of digital circuit.
167, 171, 175	NC	-	It is a non-use pin. Fix it in GND or VDD.
165 166	AVDD	-	It is a power supply supply terminal for built-in analog-to-digital converter. Supply +2.5V (analog).
176 177	AGND	-	It is a GND terminal for built-in D/A converter.
6	BUNRI	IN	It is a separation test control terminal of inside RAM. Input LOW in use usually.
90	TMC1	iN	It is a test terminal. Input LOW in use usually.
148	TMC2	IN	
4	DMCK/RF_A	IN	It is the system clock input of DVD/CD-ROM decoder. Input 10-54MHz.
189	CKCD	IN .	It is master clock of an audio system I/F block. In audio out of a CD, input 16.9MHz of reference clock.
5	DMACKI/PD4	IN	Fix unused time (unused usually) in GND or VDD.
149	VCOCLK	IN .	With system clock of spindle demodulator, it is connected to VCO of outside charge account.
161	XRESET	IN	By the input of a LOW level, it initialize the whole large scale integrated circuit system.
135	SA19	1/0	Connect address bus of central processing unit.
134	SA18		
133	SA17		
132	SA16		
129	SA15	_]	
128	SA14		
127	SA13		
126	SA12		
125	SA11		
124	SA10		
123	SA9		

_	3
_	0

2

No.	Pin name	Dir.	Pin Functions
122	SA8	IN	Connect address bus of central processing unit.
121	SA7	7	
120	SA6	1	
119	SA5	7	
118	SA4	7	
117	SA3	7	
116	SA2	7	
115	SA1	7	
114	SA0	7	
99	SAD7	1/0	Connect a data bus of central processing unit.
100	SAD6	7	
101	SAD5	7	
102	SAD4	7	
108	SAD3	7	
109	SAD2	7	
110	SAD1	7	
111	SAD0	7	
97	XSRD	IN	Be connected to a RD signal of central processing unit.
98	XSWR	IN	Be connected to a WR signal of central processing unit.
96	XSCL1	IN	It is chip select signal from central processing unit. XSRD/XSWR becomes effective at the time of LOW this signal.
95	XSWAIT	OUT	It is the WAIT output for central processing unit. This terminal must leave access from central processing unit at the time of LOW.
94	XSDREQ	OUT	It is a DMA demand for central processing unit. LOW level hip of this terminal falls down and activates DMA transfer with an edge.
93	SDACK	IN	It is DMA answer back. Data are output with HIGH this signal by SAD (7:0).
112	XIRQ10	OUT	It demand interrupt for central processing unit with LOW.
113	XIRQ11	001	Both terminals can set it with a register whether they output it.
141	FGPL/PE3	IN	Input a turn pulse from spindle motor.
147	FPWM	ОИТ	It is 7bitPWM output terminal for FG servo. It is the 3 value output of HIGH,LOW, high impedance.
146	VPWM	OUT	It is 5bitPWM output terminal for speed servo. It is the 3 value output of HIGH,LOW, high impedance.
143	PPWM	OUT	It is pulse width modulation output terminal for phase servo. It is the 3 value output of HIGH,LOW, high impedance.
142	RERR	OUT	It is control output for rough servo. It is the 3 value output of HIGH,LOW, high impedance.
31	PA7	VO.	It is general-purpose I/O port. By setting of a \$70 register, You can select a function. CDDO inputs a digital out signal from a CD decoder.
32	PA6		DIFOUT is digital audio output terminal based on IEC958.
33	PA5		BCA is terminal to input a BCA code into. BWDIN is terminal to input a WOBBLE signal into.
34	PA4		BCA/RWDIN is terminal to input a WOBBLE signal into. BCA/RWDIN terminal becomes necessary with RW revitalization machines.
35	CDDO/PA3	7	
36	DIFOUT		
196	BCA/PA1		
195	RWDIN/PA0		

С

No.	Pin name	Dir.	Pin Functions	
138	PD7/STATUS2	OUT	It output a various monitor signal (STATUS (2:0)).	
139	PD6/STATUS1	1	By setting of a \$ 70 register, You can use it as a general-purpose I/O port port.	
140	PD5/STATUS0	1	·	
151	DUTY50	OUT	It always output a pulse of duty 50%. It give reference voltage of a various PWD signal of the recovery system.	
160	ASC	OUT	it output frequency error of a sink period as a PWD pulse.	
153	APC	OUT	It output a phase error of phase locked loop as a PWD pulse.	
159	ATC	OUT	It output a direct current error of a RF signal as a PWD pulse.	
152	AFC	OUT	It output VC OCL k and frequency error of reference clock as a PWD pulse. It is the 3 value output of HIGH,LOW, high impedance.	
163	DEFECT/PE1	IN	It is the diffect signal input from the outside. Then a phase error of phase locked loop outputs this terminal in HIGH (APC), and it is done front value hold.	
162	T_DET/PC7	OUT	It output a tangential-tilt search result as a pulse width modulation pulse.	
70	DA13	OUT	It is address signal of DRAM for a VBR buffer.	
71	DA12	1		
72	DA11			
75	DA10	1		
76	DA9			
77	DA8	1		
80	DA7	1		
81	DA6	1		
82	DA5	1		
83	DA4	1	·	
86	DA3	1	· ·	
87	DA2	1		
88	DA1	1		
89	DA0	1		
39	DD15	1/0	It is a data bus of DRAM for a VBR buffer.	
42	DD14	1		
43	DD13	1		
44	DD12	1		
45	DD11	1		
46	DD10	1		
47	DD9	1		
48	DD8	1		
49	DD7	1		
55	DD6	1		
56	DD5	1		
57	DD4	1		
58	DD3	1		
59	DD2	1		
60	DD1	1		
61	DD0	1		

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D

E

No.	Pin name	Dir.	Pin Functions
69	XDRAS	OUT	It is a RAS signal of DRAM of a VBR buffer.
67	XDCAS/XDCASL	OUT	It is a CAS signal of DRAM of a VBR buffer.
66	XDOE/DQML	OUT	It is an OE signal of DRAM of a VBR buffer.
65	XDWE	OUT	It is a WE signal of DRAM of a VBR buffer.
13	SDATA7	OUT	It is a data output bus of a VIDEO_DMA channel.
14	SDATA6		Be connected to MPEG decoder.
19	SDATA5		
20	SDATA4		
21	SDATA3		
22	SDATA2		
23	SDATA1		
24	SDATA0		
29	SREQ	IN	It is a data transfer demand terminal of a VIDEO_DMA channel. Be connected to MPEG decoder. You can change polarity by setting.
25	XSACK/PC5	оит	It is a transfer reply terminal of a VIDEO_DMA channel. Be connected to MPEG decoder. Output form varies with setting.
28	XWR	OUT	It is a transfer reply terminal of a VIDEO_DMA channel. Be connected to MPEG decoder. Cutput form varies with setting.
30	XAVTRM/PC6	OUT	It is a signal to show the top of a sector of transfer data of a VIDEO_DMA channel in.
7	DSPA0/PC0	OUT	When it connects Motorola Digital Signal Processor as destination of an AUDIO_DMA
8	DSPA1/PC1	1	channel, it is the signal which gives a DMA address to Motorola Digital Signal Processor.
9	DSPA2/PC2	1	
206	ASDATA0/PB0	1/0	It is general-purpose I/O port.
205	ASDATA1/PB1	i	By setting of a \$70 register, it become a data output bus of an AUDIO_DMA channel besides a port.
204	ASDATA2/PB2	1	
203	ASDATA3/PB3	1	
202	ASDATA4/PB4	1	
201	ASDATA5/PB5	1	
200	ASDATA6/PB6	1	
199	ASDATA7/PB7		
10	XAWR	OUT	It is a transfer reply terminal of an AUDIO_DMA channel. Output form varies with setting.
11	XASACK	OUT	It is a transfer reply terminal of an AUDIO_DMA channel. Output form varies with setting.
12	ASREQ	IN	It is a transfer demand terminal of an AUDIO_DMA channel. You can change polarity by setting.
192	BCK	OUT	It is the bit clock output to DAC.
193	LRCK	OUT	It is the LRCK signal output to DAC.
194	ADATA0	OUT	It is the serial data output to DAC.
187	CDBCK	IN	It input a bit clock from a CD decoder. Prospective frequency is 2.1168MHz(48fs).
186	CDLR	IN	It input a LRCK signal from a CD decoder.

It input audio system data from a CD decoder.

It is input terminal of assistant code sink of a CD.

It is frame clock signal of a CD.

CDDT

WFCK

185

181 180 SCOR IN

IN

No.	Pin name	Dir.	Pin Functions
179	SBSO	IN	It is an assistant code data input terminal of a CD.
184	EXCK	OUT	It is a shift clock making timeliness to send data forth on a SBSO terminal.
188	C2FI/PE2	IN	It is input terminal of C2 error flag from a CD decoder.
136	FSX/STATUS4	1/0	It input a FSX signal from a CD decoder. FSX signal is 7.35Khz at normal speed with frame alignment signal of error correction of CIRC. By setting of a \$7F register, it become the internal monitor output (STATUS 4).
137	EFLG/STATUS3	I/O	It input an EFLG signal from a CD decoder. An EFLG signal is a monitor signal of error correction processing movement of CIRC. By setting of a \$7F register, it become the internal monitor output (STATUS 3).
172	AIN	IN	It is analog RF signal input terminal to built-in A/D converter.
168	VRT	IN	It is reference voltage input terminal of built-in A/D converter.
169	VRTS	OUT	Connect with VRT.
170	VRC	OUT	It is center voltage output terminal of built-in A/D converter.
174	VRB	IN	It is reference voltage input terminal of built-in A/D converter.
173	VRBS	OUT	Connect with VRB.
37	CKE/PD3	OUT	It is an Enable signal of SDCLK.
38	CSB/PD2	OUT	It is chip select signal of SDRAM.
62	SDCLK	OUT	It is a terminal outputting a movement clock of SDRAM.
68	XCASH/DOMH	OUT	When it uses DRAM of bus 16 wide bit, it is a CAS signal of high rank 8bit.
197	VREQEN/PD1	I/O	It is an Enable signal of Video-REQ.
198	AREQEN/PD0	1/0	It is an Enable signal of Audio-REQ.

5

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D

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■ PCM1742KE (JACB ASSY : IC403, IC503)

2

• D/A Converter

1

Pin Arrangement

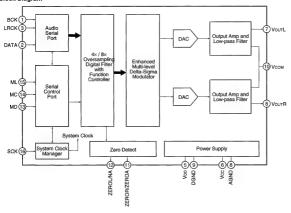
r in Arrangement						
1	вск	SCK	16			
2	DATA	ML	15			
3	LRCK	MC	14			
4	DGND	MD	13			
5	Voo	ZEROL/NA	12			
6	Vcc	ZEROR/ZEROA	11			
7	VoutL	Vсом	10			
8	VoutR	AGND	9			

Pin Function

No.	Nmae	1/0	Pin Function
1	BCK	1	Audio data bit clock input
2	DATA	-1	Audio data digital input
3	LRCK	1	L-channel and R-channel Audio data latch enable input
4	DGND	-	Digital ground
5	VDD	-	Digital power supply +3.3V
6	Vcc	-	Analog power supply +5V
7	VoutL	0	Analog output for L-channel
8	VoutR	0	Analog output for R-channel
9	AGND	-	Analog ground
10	Vcом	-	Common voltage decoupling
11	ZEROR/ZEROA	0	Zero flag output for R-channel / Zero flag output for L/R-channel
12	ZEROL/NA	0	Zero flag output for L-channel / No assign
13	MD	T	Mode control data input
14	MC	1	Mode control clock input
15	ML	- 1	Mode control latch input
16	SCK	1	System clock input

Block Diagram

С



7.3 DISC / CONTENT FORMAT PLAYBACK COMPATIBILITY

Disc / Content Format Playback Compatibility

General Disc Compatibility

· This player was designed and engineered to be compatible with software containing one or more of the following logos.













CD-RW

Super VCD" Super Audio CD²

- *1 DV-656A only
- *2 DV-45A only
- Other formats, including but not limited to the following, are not playable in this player:

Photo CD / DVD-RAM / DVD-ROM / CD-ROM

(except those that contain MP3 files formatted as specified in the "Compressed Audio Compatibility" section)

 DVD-R/RW and CD-R/RW discs (Audio CDs and Video CDs) recorded using a DVD Recorder CD Recorder or Personal Computer may not be playable on this machine. This may be caused by a number of possibilities, including but not limited to: the type of disc used: the type of recording; or damage, dirt or condensation on either the disc or the player s pick-up lens.

CD-R/RW Compatibility

- This unit will play CD-R and CD-RW discs recorded in CD Audio, Video CD, or MP3 audio formatting. However, any other content may cause the disc not to play, or create noise/distortion in the output.
- · This unit cannot record CD-R or CD-RW
- · Un-finalized CD-R/RW discs recorded in CD Audio can be played, but not all Table of Contents (playing time, etc..) will be displayed

DVD-R/RW Compatibility

- . This unit will play DVD-R/RW discs that were recorded using the DVD Video format.
- · This unit will play DVD-RW discs that were recorded using the Video Recording format.
- · This unit cannot record DVD-R/RW discs
- · Un-finalized DVD-R/RW discs cannot be played in this player.

n

7.4 CLEANING

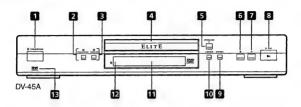


Before shipping out the product, be sure to clean the following positions by using the prescribed cleaning tools:

Position to be cleaned	Cleaning tools
Pickup leneses	Cleaning liquid: GEM1004 Cleaning paper: GED-008

8 PANEL FACILITIES

Front panel



1 & STANDBY/ON

Press to switch the player on or into standby

2 PROGRESSIVE button/indicator

Press to switch the progressive video output mode between progressive and interlace. The indicator lights in progressive scan mode.

3 VIDEO OFF button/indicator

Press to switch the video output on/off. The indicator lights when the video output is switched off (when listening to a DVD-Audio disc. for example)

4 Disc trav

5 ≜ OPEN/CLOSE

Press to open or close the disc tray

playback by pressing ► (play))

Press to pause playback. Press again to restart

Press to stop the disc (you can resume

3 ▶

Press to start or resume playback

9 ---

- · Press and hold for fast forward scanning
- · Press to jump to the next chapter or track

10 | 44 44

- · Press and hold for fast reverse scanning
- Press to jump back to the beginning of the current chapter or track, then to previous chapters/tracks

11 Display

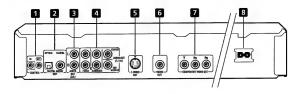
12 Remote control sensor

The remote control has a range of up to about 23ft. (7m)

13 RW

Ths mark indicates compatibility with DVD-RW discs receorded on a DVD recorder in Video Recording mode.

Rear panel



When connecting this player up to your TV, AV receiver or other components, make sure that all components are switched off and unplugged.

1 CONTROL IN / OUT

For passing remote control signals to other Pioneer components.

2 DIGITAL AUDIO OUT – OPTICAL / COAXIAL

Digital audio outputs for connection to a PCM, Dolby Digital, DTS and/or MPEG-compatible AV receiver.

3 AUDIO OUT (2ch)

Two channel analog audio outputs for connection to your TV, AV receiver or stereo system.

4 AUDIO OUT (5.1ch)

Multichannel analog audio outputs for connection to an AV receiver with multichannel inputs.

5 S (S-Video output)

S-Video output(s) that you can use instead of the video output described in **6** below.

6 VIDEO OUT

Standard video output(s) that you can connect to your TV or AV receiver using the supplied audio/video cable.

7 COMPONENT VIDEO OUT

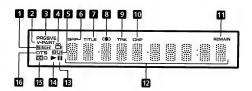
High quality video output for connection to a TV, monitor or AV receiver that has component video inputs.

Connect using a commercially available three-way component video cable. Be careful to match the colors of the jacks and cables for correct connection.

8 ACIN

Connect the supplied power cord here, then plug into a power outlet.

Display



1 5.1CH

Lights when analog 5.1 channel output is selected

2 V-PART

Lights when playing a video part of a DVD disc

3 PRGSVE

Lights when the video output is progressive scan

4 20

Lights during multi-angle scenes on a DVD disc

5 GUI (Graphical User Interface)

Lights when a menu is displayed on-screen

6 GRP

Indicates that the character display is showing a DVD-Audio group number

7 TITLE

Indicates that the character display is showing a DVD-Video title number

R con

Lights when DV/TruSurround is active

9 TRK

Indicates that the character display is showing a track number

10 CHP

Indicates that the character display is showing a DVD chapter number

11 REMAIN

Lights when the character display is showing the time or number of tracks/titles/chapters remaining

12 Character display

13 II

Lights when a disc is paused

14 ▶

Lights when a disc is playing

15 DDD

Lights when a Dolby Digital soundtrack is playing

16 DTS

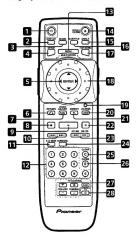
Lights when a DTS soundtrack is playing

С

Remote control [DV-45A]



 Buttons 6 thru 9 and 20 thu 22 glow slightly in the dark for ease of use.



1 & (STANDBY/ON)

Press to switch the player on or into standby

2 DISPLAY

Press to display information about the disc playing

3 AUDIO

Press to select the audio channel or language

4 SETUP

Press to display (or exit) the on-screen display

5 ENTER & Joystick

Use to navigate on-screen displays and menus. Press **ENTER** to select an option or execute a command

6 & (RETURN)

Press to return to a previous menu screen

7 V.ADJ (VIDEO ADJUST)

Press to display the Video Adjust menu

8

Press to stop the disc (you can resume playback by pressing ► (play))

۹ ۱

Press to start or resume playback

10

Press to jump to the start of the previous / next chapter / track

11 PLAY MODE

Press to display the Play Mode menu (You can also get to the Play Mode menu by pressing **SETUP** and selecting **Play Mode**)

12 Number buttons

13 MENU

Press to display a DVD disc menu, or the Disc Navigator if a DVD-RW, CD, Video CD or MP3 disc is loaded

14 ▲ OPEN/CLOSE

Press to open or close the disc tray

15 ANGLE

Press to change the camera angle during DVD multi-angle scene playback

16 SUBTITLE

Press to select a subtitle display

17 TOP MENU

Press to display the top menu of a DVD disc

18 MULTI DIAL

Use for scanning and slow motion control

28 & TV Press & TV to turn the TV s power on or put

in to standby

19 Jog indcator

Lights when multi dial is in jog mode

20 JOG (JOG MODE)

Press to put switch jog mode on/off. When on, use the **MULTI DIAL** to advance or reverse frames

21 FL (DIMMER)

Press to change the display brightness

22 H

Press to pause playback; press again to restart

23 ◀◀ and ◀/◀II / ▶▶ and II▶/I▶

Use for reverse / forward slow motion playback, frame reverse / advance and reverse / forward scanning.

24 SURROUND

Press to activate/switch off DOV/TruSurround.

25 CLEAR

Press to clear a numeric entry

26 ENTER

Press to select an option or execute a command

27 TV CONTROL buttons

VOLUME

Use to adjust the volume

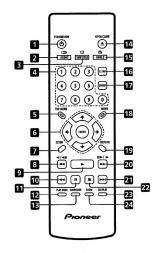
CHANNEL

Use to select TV channel

FUNC

Press FUNC to select the TV for remote control operation

Remote control [DV-656A]



⊕ STANDBY/ON

Press to switch the player on or into standby

2 AUDIO

Press to select the audio channel or language

3 SUBTITLE

Press to select a subtitle display

4 Number buttons

5 TOP MENU

Press to display the top menu of a DVD disc

6 ENTER & cursor control buttons

Use to navigate on-screen displays and menus. Press **ENTER** to select an option or execute a command

7 SETUP

Press to display (or exit) the on-screen display

8 **◄◄** and **◄**/**◄**II

Use for reverse slow motion playback, frame reverse and reverse scanning.

, ,

Press to start or resume playback

10

Press to jump to the beginning of the current chapter or track, then to previous chapters/ tracks

11 II

Press to pause playback; press again to restart

12 PLAY MODE

Press to display the Play Mode menu (You can also get to the Play Mode menu by pressing **SETUP** and selecting **Play Mode**)

13 SURROUND

Press to activate/switch off DV/TruSurround

14 ▲ OPEN/CLOSE

Press to open or close the disc tray

15 ANGLE

Press to change the camera angle during DVD multi-angle scene playback

16 CLEAR

Press to clear a numeric entry

17 ENTER

Use to select menu options, etc. (works exactly the same as the **ENTER** button in 6 above)

18 MENU

Press to display a DVD disc menu, or the Disc Navigator if a DVD-RW, CD, Video CD or MP3 disc is loaded

19 RETURN

Press to return to a previous menu screen

20 ▶► and II►/I►

Use for forward slow motion playback, frame advance and forward scanning.

21 ▶▶

Press to jump to the next chapter or track

22 🔳

Press to stop the disc (you can resume playback by pressing ► (play))

23 DISPLAY

Press to display information about the disc

24 ZOOM

Press to change the zoom level

В